Reconfigurable Computing: Readings
(CPRE 583, Fall 2011)

Note: For some links you must run your web browser from an on-campus computer.

1. An Overview of Reconfigurable Hardware in Embedded Systems (2006)
http://portal.acm.org/citation.cfm?id=1288236

2. Reconfigurable Computer Origins: The UCLA Fixed-Plus-Variable (F+V) Structure Computer (2002)
http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=1114865

3. PipeRench: A Reconfigurable Architecture and Compiler (2000)
http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=839324

4. The Garp Architecture and C Compiler (2000)
http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=839323

5. Instruction Set Innovations for the Convey HC-1 Computer (2010)
http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5446252

6. High-Performance Heterogeneous Computing with the Convey HC-1 (2010)
http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5624681

7. Design Patterns for Reconfigurable Computing (2004)
http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=1364613

8. A new species of hardware (2000): (See webpage for scanned original that readable)
http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=825661

9. Real-world applications of analog and digital evolvable hardware (1999)
http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=788492

10. Generalized Disjunction Decomposition for Evolvable Hardware (2006)
http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=1703646

11. Achieving High Performance with FPGA-Based Computing (2007)
http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4133996

12. Computing Models for FPGA-Based Accelerators (2008)
http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4653203