EE 330 Lecture 22

- Small Signal Analysis
- Small Signal Modelling



But – this expression gives little insight into how large the gain is ! Can the gain be made arbitrarily large by simply making R large? Observe increasing R with W,L, and V_{SS} fixed will change Q-point Difficult to answer this question with the information provided !



$$\mathbf{I}_{DQ} = \frac{\mu \mathbf{C}_{OX} \mathbf{W}}{2L} \left(\mathbf{V}_{INQ} - \mathbf{V}_{SS} - \mathbf{V}_{T} \right)^{2}$$

But recall:

Thus, substituting from the expression for I_{DQ} we obtain

$$A_{v} = -\frac{2I_{DQ}R}{\left[V_{INQ}-V_{SS}-V_{T}\right]} = -\frac{2I_{DQ}R}{\left[V_{GSQ}-V_{T}\right]}$$

Small signal analysis example



$$A_{v} = -\frac{2I_{DQ}R}{\left[V_{GSQ}-V_{T}\right]}$$

- tedious.
- This approach becomes unwieldy for even slightly more complicated circuits
- A much easier approach based upon the development of small signal models will provide the same results, provide more insight into both analysis and design, and result in a dramatic reduction in computational requirements



Note presence of second harmonic distortion term !

Small signal analysis example



 $V_{OUT} = V_{OUTDC} + \{A_V V_M \sin \omega t\} + \{A_2 V_M \cos 2\omega t\}$

Small signal analysis example



Total Harmonic Distortion: Recall, if $x(t) = \sum_{k=0}^{\infty} b_k \sin(k\omega T + \phi_k)$ then $THD = \frac{\sqrt{\sum_{k=2}^{\infty} b_k^2}}{|b_1|}$

Thus, for this amplifier, as long as M₁ stays in the saturation region

$$THD = \frac{\sqrt{\left(A_2 V_M\right)^2}}{\left|A_V V_M\right|} = \frac{A_2}{\left|A_V\right|} = \frac{\frac{\mu C_{OX} W}{4L} R V_M}{\frac{\mu C_{OX} W}{L} R (V_{GSQ} - V_T)} = \frac{V_M}{4 (V_{GSQ} - V_T)}$$

Distortion will be small for $V_M << (V_{GSQ} - V_T)$

Distortion will be much worse (larger and more harmonic terms) if M₁ leaves saturation region.

Consider the following MOSFET and BJT Circuits



- Analysis was very time consuming
- Issue of operation of circuit was obscured in the details of the analysis

One of the most widely used amplifier architectures



Consider the following MOSFET and BJT Circuits

One of the most widely used amplifier architectures

Small signal analysis using nonlinear models



Small signal analysis using nonlinear models

$$V_{\text{IN}}(t) \bigoplus_{V_{\text{CC}}} V_{\text{OUT}} = V_{\text{CC}} - J_{\text{S}}A_{\text{E}}R_{1}e^{\frac{V_{\text{M}}\sin(\omega t) + V_{\text{M}}\omega}{V_{t}}}$$

$$V_{\text{OUT}} = V_{\text{CC}} - J_{\text{S}}A_{\text{E}}R_{1}e^{\frac{V_{\text{M}}\sin(\omega t) + V_{\text{M}}\omega}{V_{t}}}$$

$$V_{\text{OUT}} = V_{\text{CC}} - J_{\text{S}}A_{\text{E}}R_{1}e^{\frac{V_{\text{M}}\sin(\omega t)}{V_{t}}}$$

$$Recall that if x is small e^{c} \cong 1 + \varepsilon \qquad (truncated Taylor's series)$$

$$V_{\text{IN}} = V_{\text{INQ}} + V_{\text{M}}\sin\omega t$$

$$V_{\text{OUT}} \cong V_{\text{CC}} - J_{\text{S}}A_{\text{E}}R_{1}e^{\frac{V_{\text{M}}\omega}{V_{t}}}\left(1 + \frac{V_{\text{M}}\sin(\omega t)}{V_{t}}\right)$$

$$V_{\text{M}} \text{ is small} \qquad \therefore \quad V_{\text{OUT}} \cong V_{\text{CC}} - J_{\text{S}}A_{\text{E}}R_{1}e^{\frac{V_{\text{M}}\omega}{V_{t}}}\left(1 + \frac{V_{\text{M}}\sin(\omega t)}{V_{t}}\right)$$

$$V_{\text{OUT}} \cong \left[V_{\text{CC}} - J_{\text{S}}A_{\text{E}}R_{1}e^{\frac{V_{\text{M}}\omega}{V_{t}}}\right] - \frac{J_{\text{S}}A_{\text{E}}R_{1}e^{\frac{V_{\text{M}}\omega}{V_{t}}}}{V_{t}}V_{\text{M}}\sin(\omega t)$$

Small signal analysis using nonlinear models



Comparison of Gains for MOSFET and BJT Circuits



Observe A_{VB} >> A_{VM} Due to exponential-law rather than square-law model

Operation with Small-Signal Inputs

- Analysis procedure for these simple circuits was very tedious
- This approach will be unmanageable for even modestly more complicated circuits
- Faster analysis method is needed !

Small-Signal Analysis



- Will commit next several lectures to developing this approach
- Analysis will be MUCH simpler, faster, and provide significantly more insight
- Applicable to many fields of engineering

Small-Signal Analysis



Operation with Small-Signal Inputs

Why was this analysis so tedious?

Because of the nonlinearity in the device models

What was the key technique in the analysis that was used to obtain a simple expression for the output (and that related linearly to the input)?

$$V_{\text{OUT}} = V_{\text{CC}} - J_{\text{S}}A_{\text{E}}R_{1}e^{\frac{V_{beQ}}{V_{t}}}e^{\frac{V_{\text{M}}\sin(\omega t)}{V_{t}}}$$
$$V_{\text{OUT}} \cong \left[V_{\text{CC}} - I_{cQ}R_{1}\right] - \left(\frac{I_{cQ}R_{1}}{V_{t}}\right)V_{\text{M}}\sin(\omega t)$$

Linearization of the nonlinear output expression at the operating point

Operation with Small-Signal Inputs $I_{CQ} = J_{S}A_{E}e^{\frac{V_{beQ}}{V_{t}}}$ $V_{out} \cong \begin{bmatrix} V_{cc} - I_{cQ}R_{1} \end{bmatrix} - \begin{bmatrix} I_{cq}R_{1} \\ V_{i} \end{bmatrix} V_{M}sin(\omega t)$ Ss Voltage Gain

Small-signal analysis strategy

- 1. Obtain Quiescent Output (Q-point)
- 2. Linearize circuit at Q-point instead of linearize the nonlinear solution (this will be done by linearizing each component in the circuit)
- 1. Analyze linear "small-signal" circuit
- 2. Add quiescent and small-signal outputs to obtain good approximation to actual output







Relationship is nearly linear in a small enough region around Q-point Region of linearity is often quite large Linear relationship may be different for different Q-points



Relationship is nearly linear in a small enough region around Q-point Region of linearity is often quite large Linear relationship may be different for different Q-points



Device Behaves Linearly in Neighborhood of Q-Point Can be characterized in terms of a small-signal coordinate system







- Linearized model for the nonlinear function y=f(x)
- Valid in the region of the Q-point
- Will show the small signal model is simply Taylor's series expansion of f(x) at the Q-point truncated after first-order terms

Observe:

$$y - y_{\alpha} = \frac{\partial f}{\partial x}\Big|_{x = x_{\alpha}} (x - x_{\alpha}) \longrightarrow y_{ss} = \frac{\partial f}{\partial x}\Big|_{x = x_{\alpha}} x_{ss} \int_{y_{\alpha}}^{y} \int_{y_{\alpha}}^{\alpha - point} \int_{x = x_{\alpha}}^{y} y_{\alpha} = f(x_{\alpha})$$

$$y = f(x_{\alpha}) + \frac{\partial f}{\partial x}\Big|_{x = x_{\alpha}} (x - x_{\alpha})$$
Recall Taylors Series Expansion of nonlinear function (at expansion point x₀)
$$y = f(x_{0}) + \sum_{k=1}^{\infty} \left(\frac{1}{k!} \frac{df}{dx}\Big|_{x = x_{0}} (x - x_{0})^{k}\right)$$
Truncating after first-order terms (and defining "o" as "Q")
$$y = f(x_{\alpha}) + \frac{\partial f}{\partial x}\Big|_{x = x_{\alpha}} (x - x_{\alpha}) \longrightarrow y_{ss} = \frac{\partial f}{\partial x}\Big|_{x = x_{\alpha}} x_{ss}$$

Mathematically, linearized model is simply Taylor's series expansion of the nonlinear function f at the Q-point truncated after first-order terms with notation $x_Q = x_0$



How can a **<u>circuit</u>** be linearized at an operating point as an alternative to linearizing a nonlinear function at an operating point?

Consider arbitrary nonlinear one-port network



Arbitrary Nonlinear One-Port



Arbitrary Nonlinear One-Port







Linear small-signal model:

$$\boldsymbol{i} = y \boldsymbol{v}$$

A Small Signal Equivalent Circuit:



- The small-signal model of this 2-terminal electrical network is a resistor of value 1/y or a conductor of value y
- One small-signal parameter characterizes this one-port but it is dependent on Qpoint
- This applies to **ANY** nonlinear one-port that is differentiable at a Q-point (e.g. a diode)

End of Lecture 22