EE 330 Quiz #02

Name: Score

Grader:

Briefly answer the following questions:

1. Roughly how big is the solid state electronics industry (rounded to the nearest $100B): ­­\_\_\_$350B\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_
2. In the IEEE salary survey, the median salary for solid state electronics engineers is about: \_\_\_$165K\_\_\_\_\_\_,
3. Alternatively, that is: **near top**, near middle, near bottom.
4. In the state of the art CMOS technology, the feature size is: 7nm
5. In the past several decades, the feature size, the #transistors, and so on have been scaling according to the Moore’s law.
6. In the most recent CPU, GPU, FPGA chips, how many transistors are integrated in a single chip: 10s of billions
7. The largest semiconductor company is: Samsung, Intel
8. Name one large semiconductor company whose CEO is/was a graduate from our department: TI, Marvell, Maxim
9. When designing a chip, the most important thing is make money
10. If a chip design has 90% hard fault yield and 80% soft fault yield, what is the overall yield: 72%