

Features of Square Law Mixers

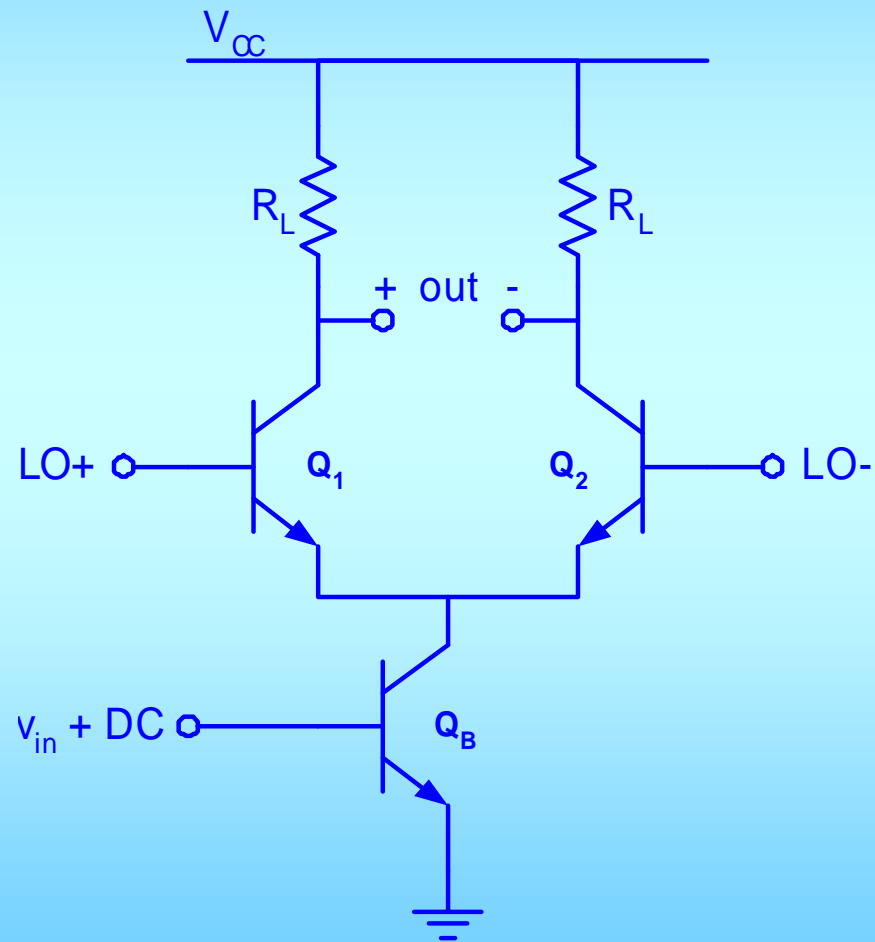
- Noise Figure: The square law MOSFET mixer can be designed to have very low noise figure.
- Linearity: true square law MOSFET mixer produces only DC, original tones, difference, and sum tones
- The corresponding BJT mixer produces a host of non-linear components due to the exponential function
- Power Dissipation: The square law mixer can be designed with very low power dissipation.
- Power Gain: Reasonable power gain can be achieved through the use of square law mixers.
- Isolation: Square law mixers offer poor isolation from LO to RF port. This is by far the biggest short coming of the square law mixers.

Mixer performance analysis

- Analyze major metrics
 - Conversion gain
 - Port isolation
 - Noise figure/factor
 - Linearity, IIP3
- Gain insights into design constraints and compromise

Common Emitter Mixer

- Single-ended input
- Differential LO
- Differential output
- Q_B provides gain for v_{in}
- Q_1 and Q_2 steer the current left and right at ω_{LO}



Common Emitter Mixer

- Conversion gain

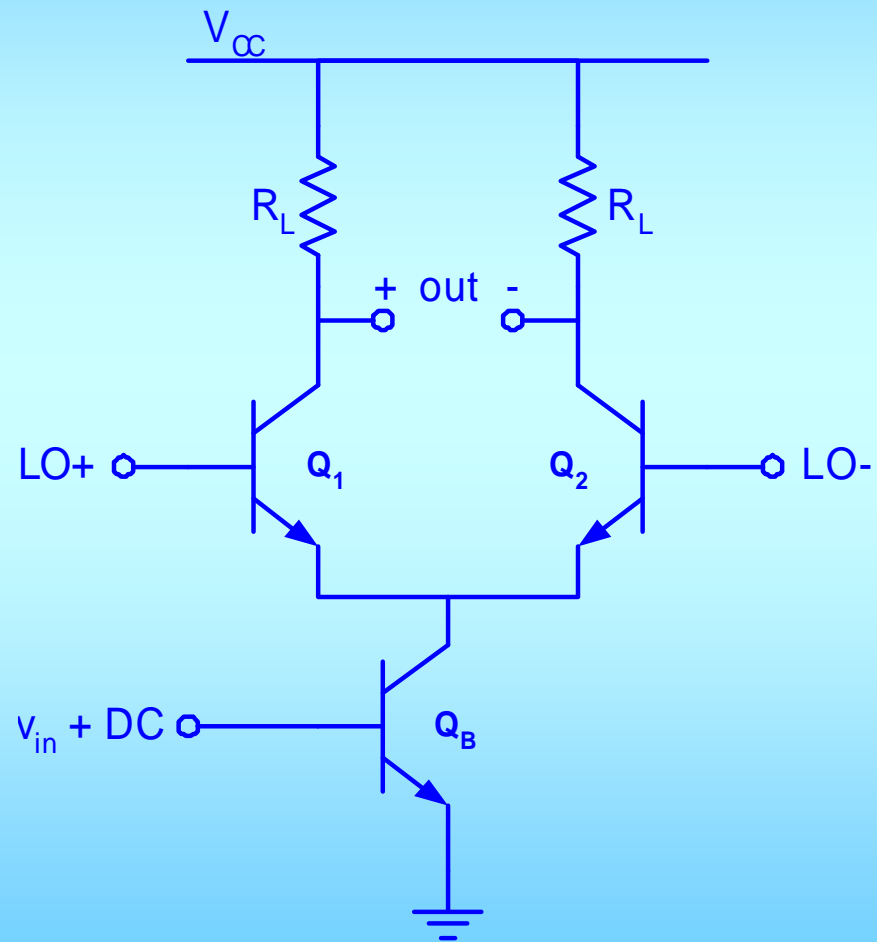
Two output component:

$$V_{\text{out1}} = \pm g_m v_{\text{in}} R_L$$

$$V_{\text{out2}} = \pm I_{Q_B \text{DC}} R_L$$

IF signal is the $\omega_{\text{RF}} - \omega_{\text{LO}}$ component in V_{out1}

So gain = ?



Common Emitter Mixer

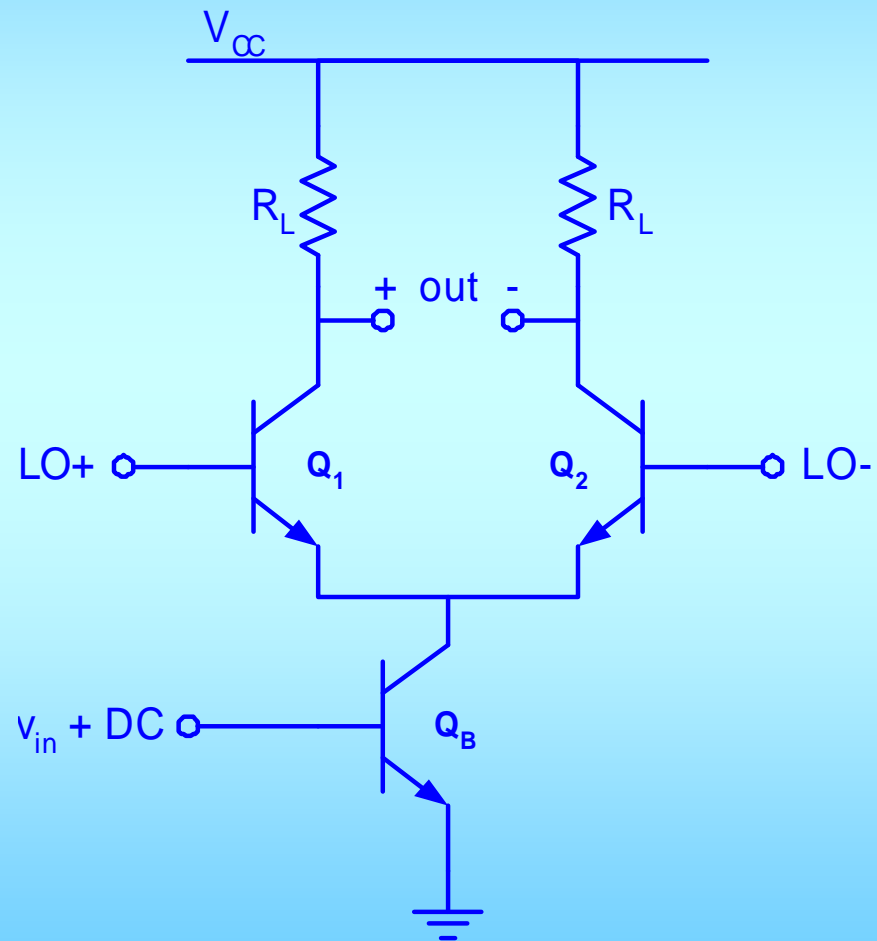
- Port isolation

At what frequency is V_{out2} switching?:

$$V_{out2} = \pm I_{Q_B DC} R_L$$

$$V_{out2} = SW(\omega_{LO}) I_{Q_B DC} R_L$$

This is feed through from LO to output

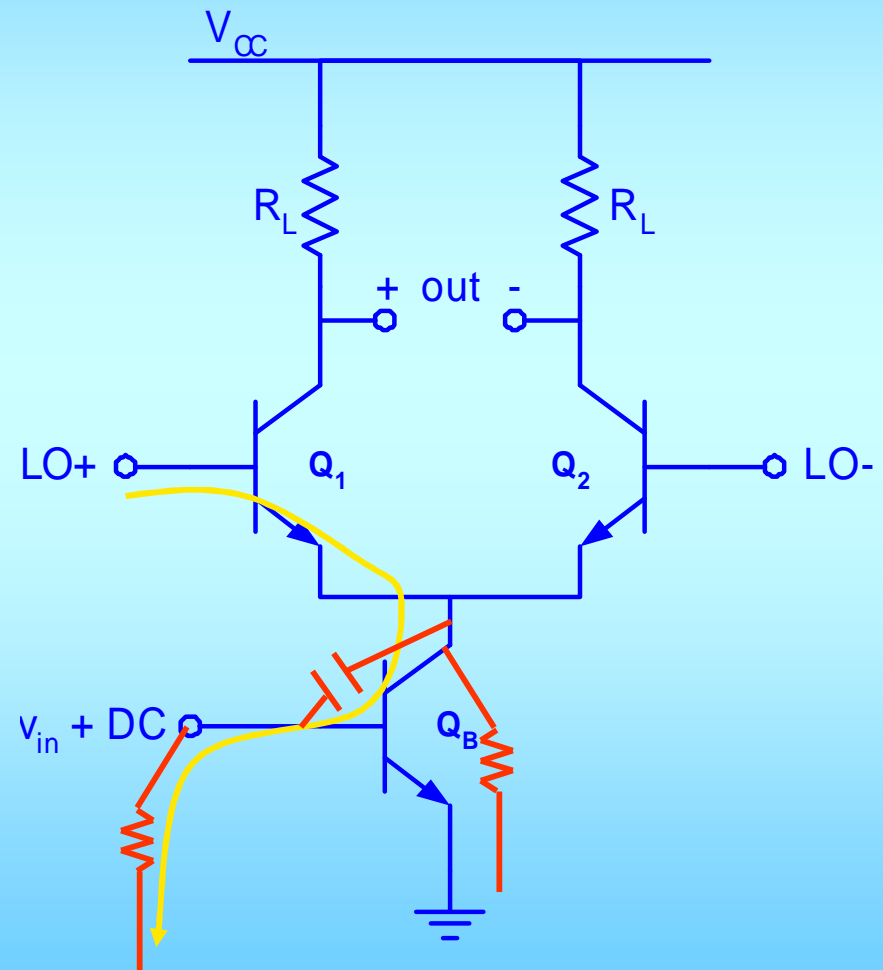


Common Emitter Mixer

- Port isolation

How about LO to RF?

This feed through is much smaller than LO to output

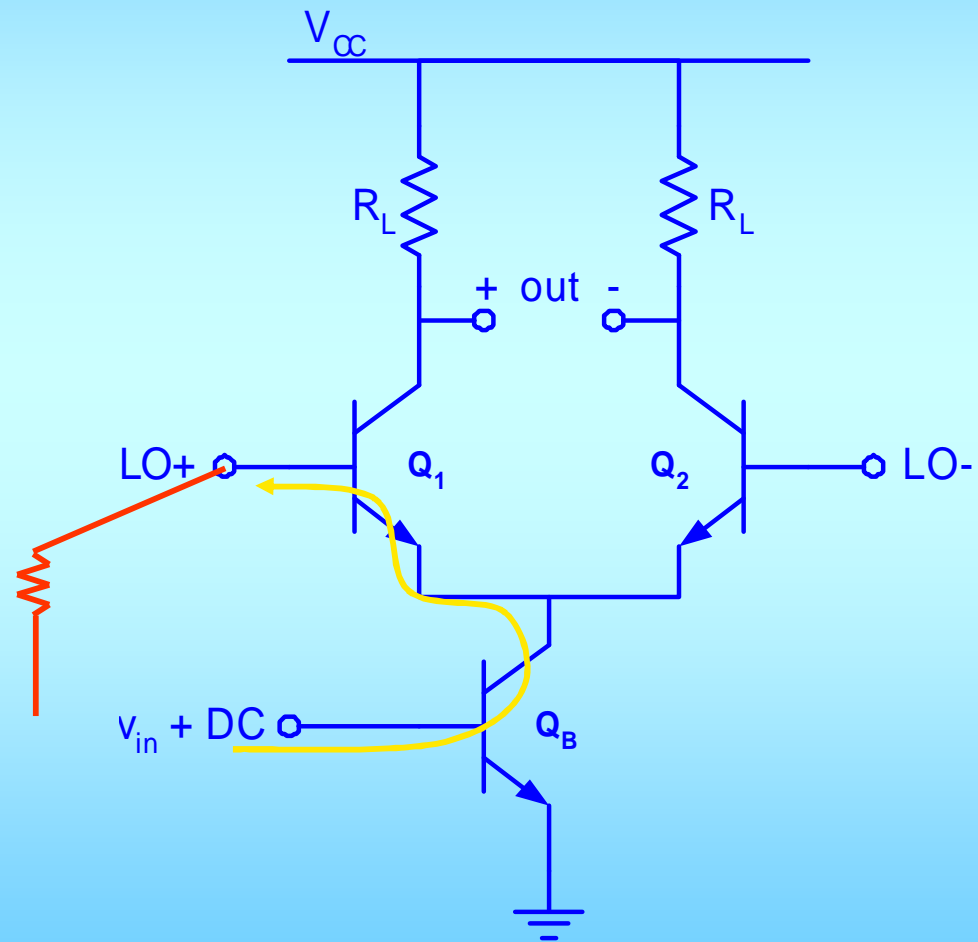


Common Emitter Mixer

- Port isolation

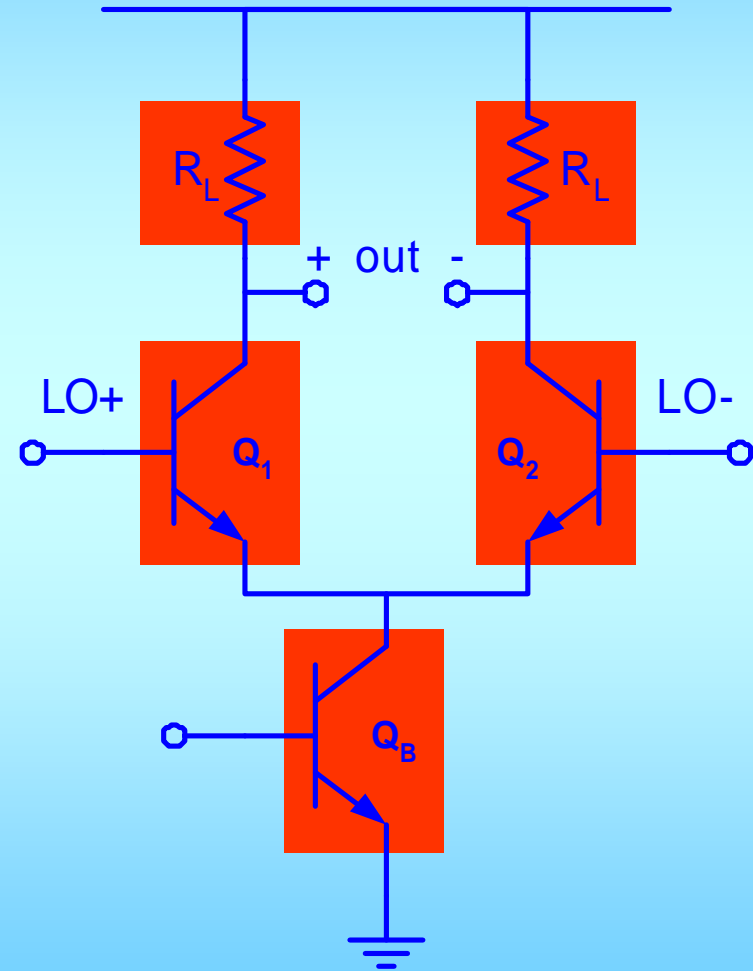
How about RF to LO?

If LO is generating a square wave signal, its output impedance is very small, resulting in small feed through from RF to LO to output.



Common Emitter Mixer

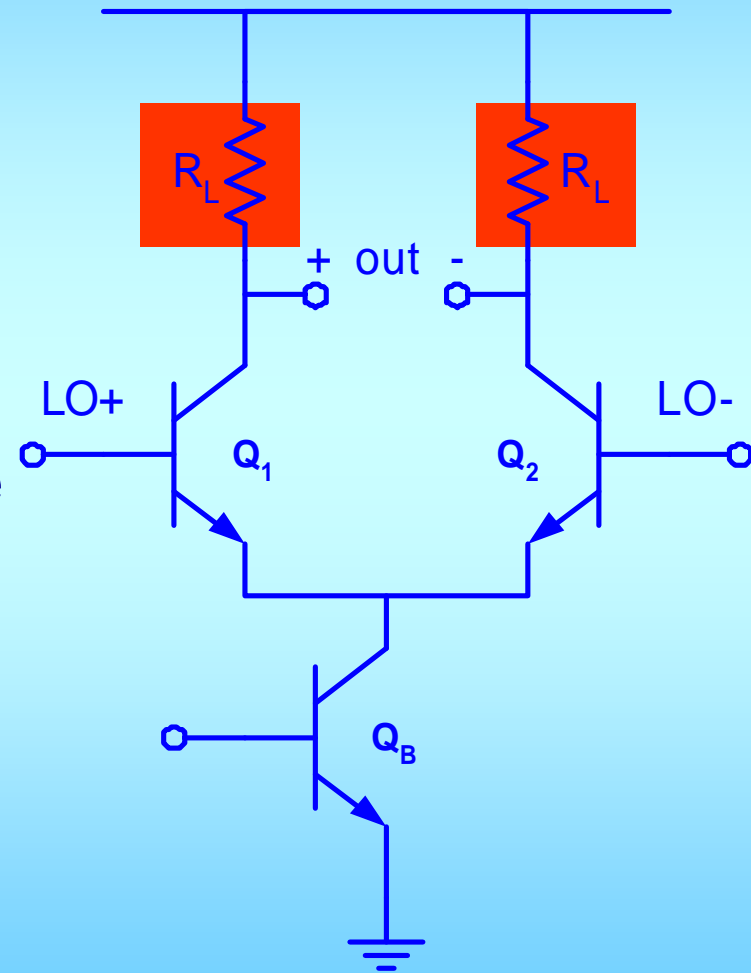
- Noise Components:
 1. Noise due to loads
 2. Noise due to the input transistor (Q_B)
 3. Noise due to switches (Q_1 and Q_2)



Common Emitter Mixer

1. Noise due to loads:

- Each R_L contributes $V_{RL}^2 = 4kTR_L\Delta f$
- Since they are uncorrelated with each other, their noise power's add
- Total contribution of R_L 's: $V_{oRL}^2 = 8kTR_L\Delta f$

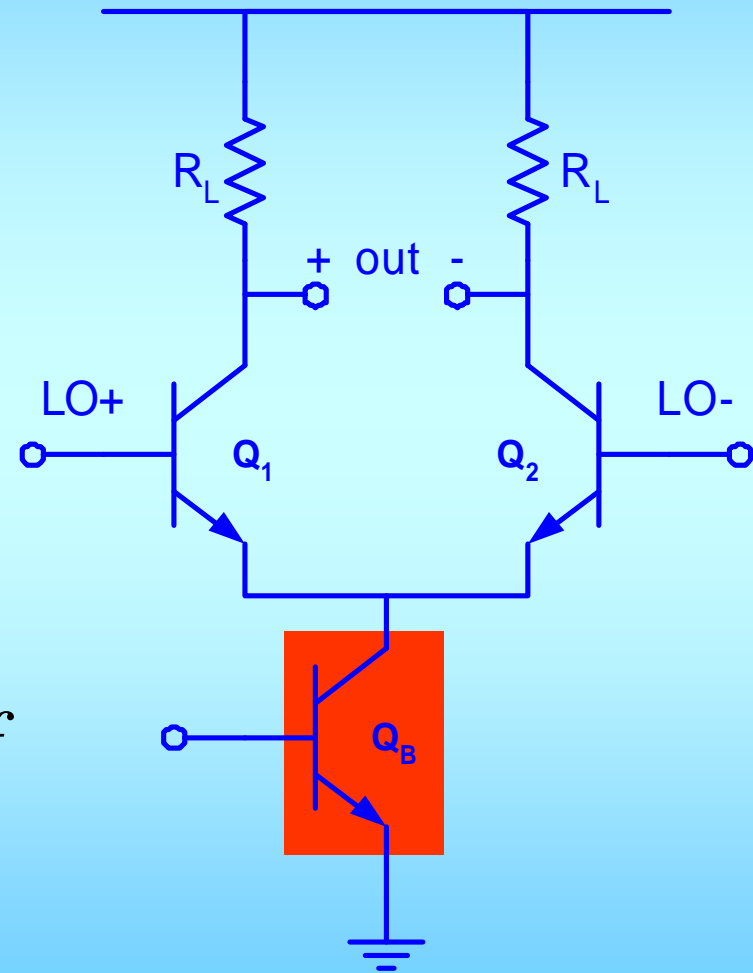


Common Emitter Mixer

2. Noise due input transistor (the transducer):

- From BJT device model, equivalent input noise voltage of a CE amplifier is:

$$\overline{v_{in(CE)}^2} = 4kT \left(r_b + \frac{1}{2g_m} \right) \Delta f$$

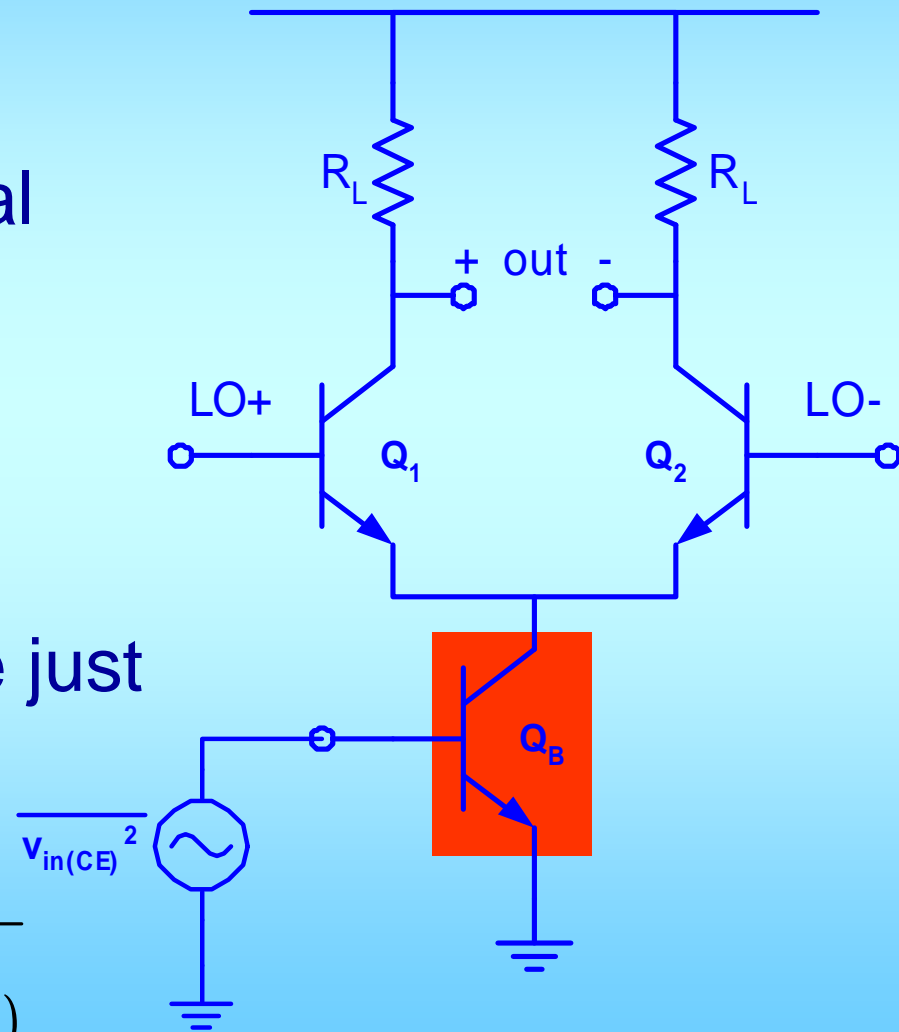


Common Emitter Mixer

2. Noise due to input transistor:

- If this is a differential amplifier, QB noise would be common mode
- But Q1 and Q2 just switching, the noise just appears at either terminal of out:

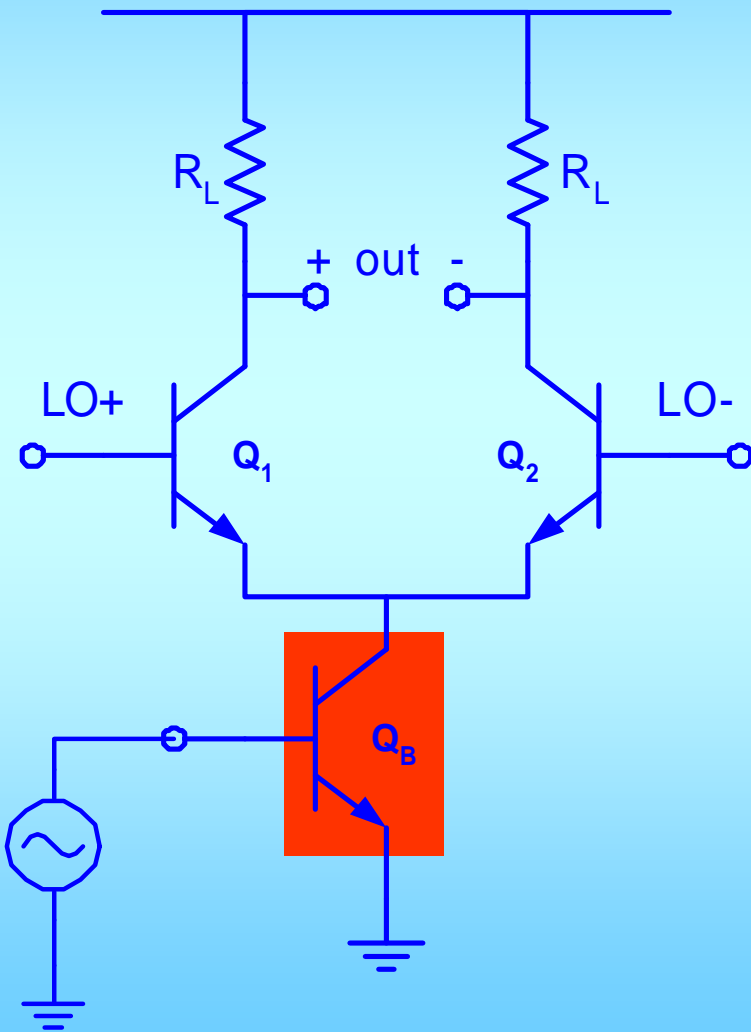
$$\overline{v_{out, Q_B}^2} = (gain)^2 \cdot \overline{v_{in(CE)}^2}$$



Common Emitter Mixer

2. Noise due to input transistor:

- Noise at the two terminals dependent?
- Accounted for by incorporating a factor "n".



$$\overline{v_{out,Q_B}^2} = n \cdot (gain)^2 \cdot \overline{v_{in(CE)}^2}$$

$$\overline{v_{out,Q_B}^2} = (g_m R_L)^2 \cdot 4kT \left(r_b + \frac{1}{2g_m} \right) \Delta f \overline{v_{in(CE)}^2}$$

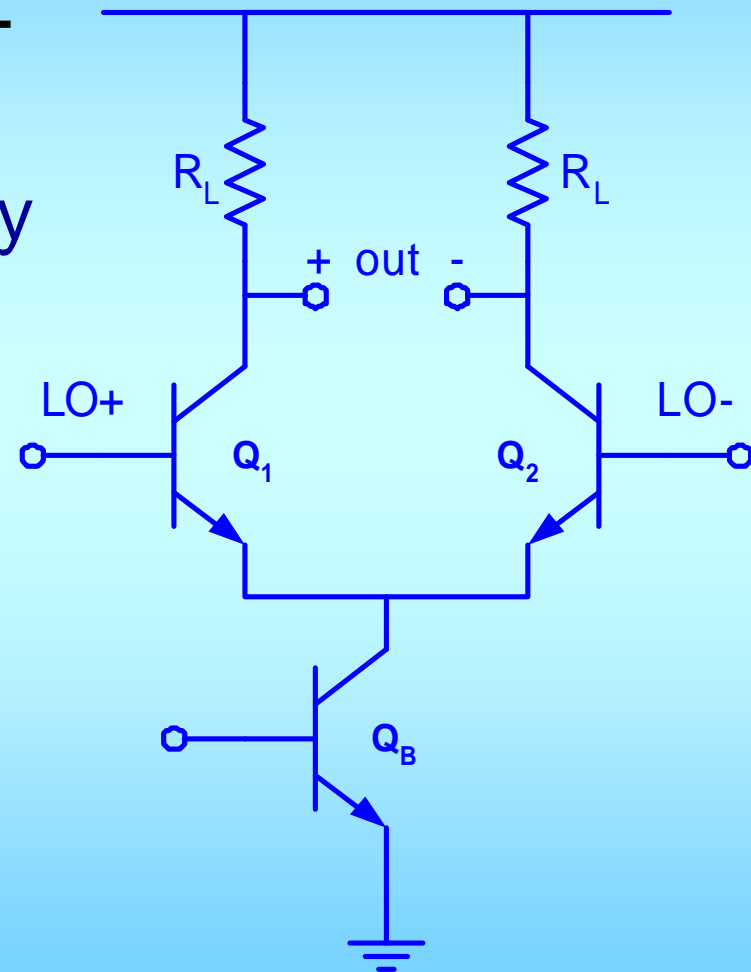
Common Emitter Mixer

- Total Noise due to R_L and Q_B :
 - If we assume r_b is very small:

$$\frac{\overline{v_T^2}}{\Delta f} \cong 8kTR_L \left(1 + \frac{g_m R_L}{4} \right)$$

When:

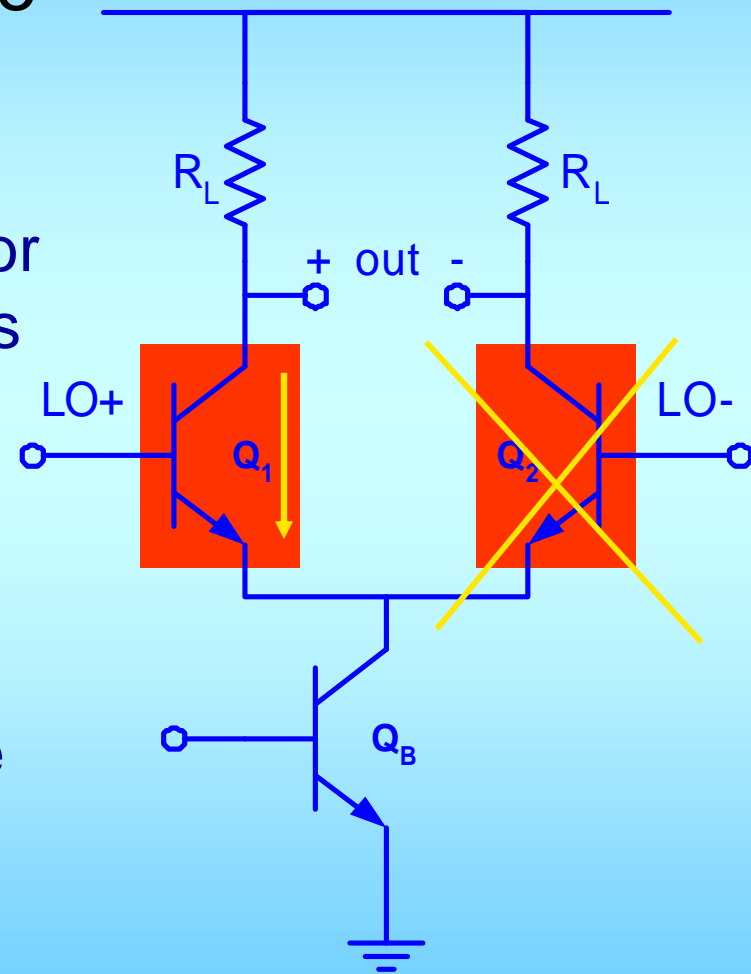
$$r_b \ll 1/(2g_m) \text{ and} \\ n=1$$



Common Emitter Mixer

3. What about the noise due to switches?

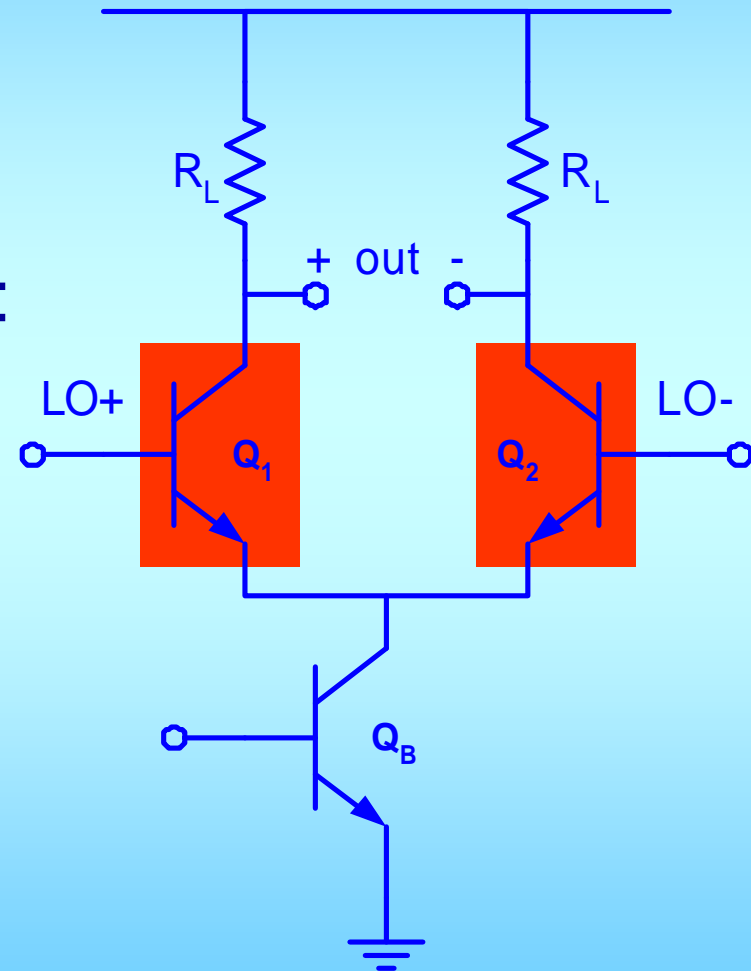
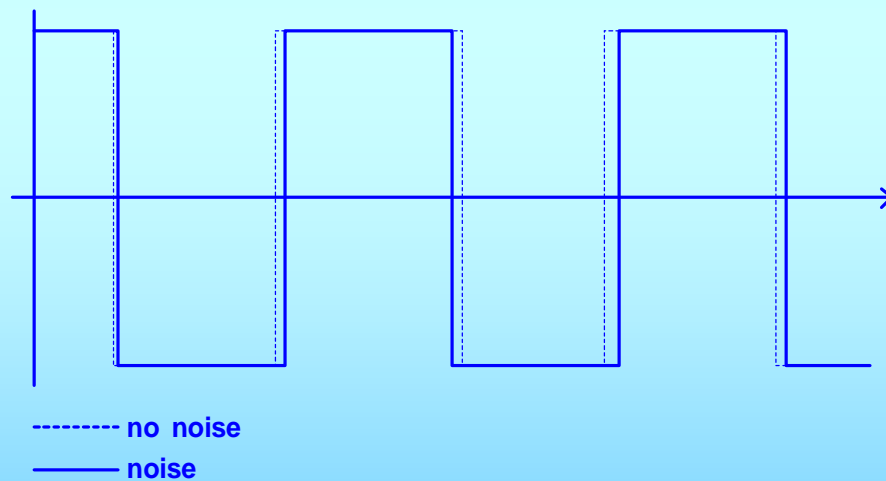
- When Q2 is off and Q1 is on, acting like a cascode or more like a resistor if LO is strong
- Can show that Q1's noise has little effect on V_{out}
- $V_{E1} \sim V_{C1}$, V_{BE1} has similar noise as V_{C1} , which cause jitter in the time for Q1 to turn off if the edges of LO are not infinitely steep



Common Emitter Mixer

3. What about the noise due to switches:

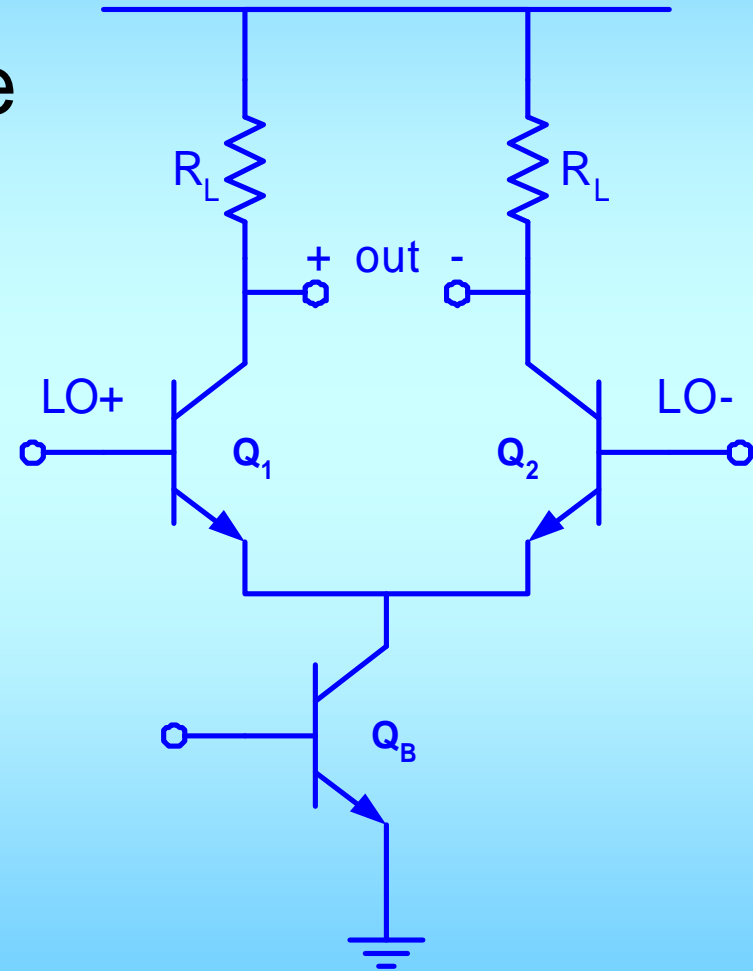
- Transition time "jitter" in the switching signal:



Effect is quite complex, quantitative analysis later

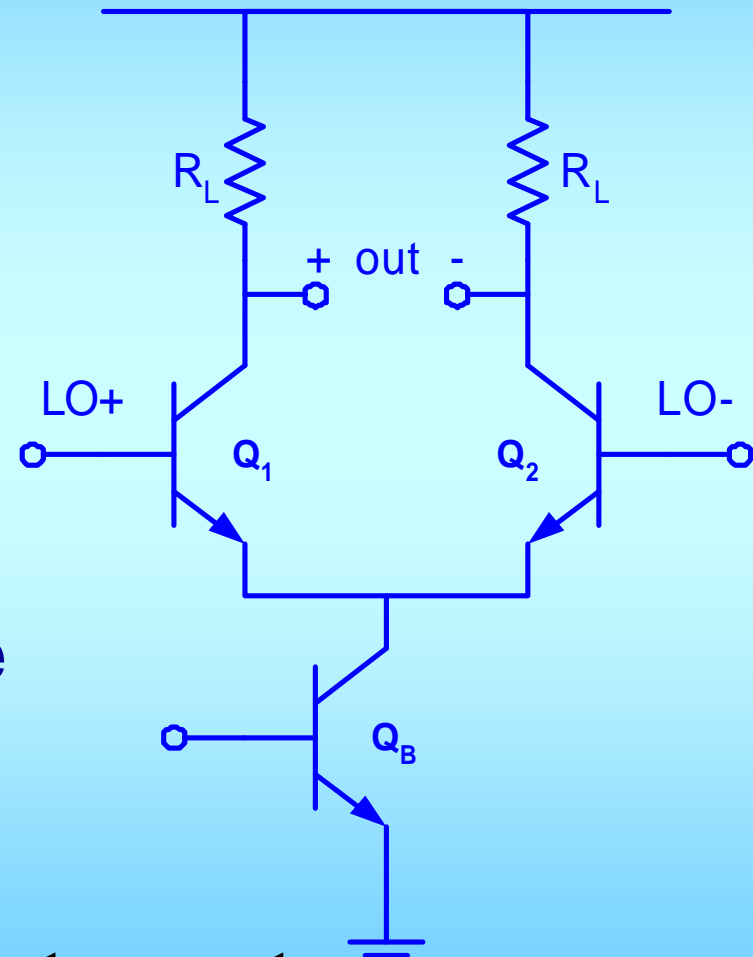
Common Emitter Mixer

- How to improve Noise Figure of mixer:
 - Reduce R_L
 - Increase g_m and reduce r_b of Q_B
 - Faster switches
 - Steeper rise or fall edge in LO
 - Less jitter in LO



Common Emitter Mixer

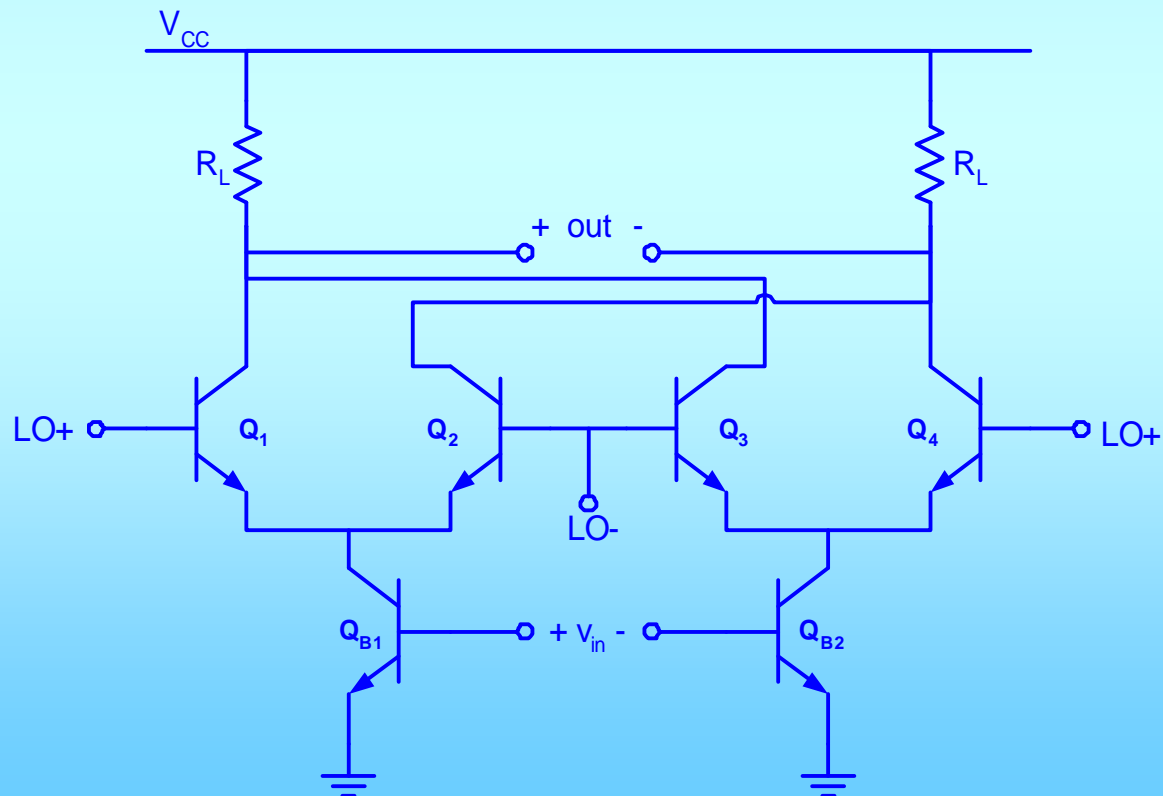
- IP3:
 - The CE input transistor (Q_B) converts v_{in} to I_{in}
 - BJTs cause 3rd-order harmonics
 - Multiplying by R_L is linear operation
 - Q_1 & Q_2 only modulate the frequency
 - $\therefore IP3_{\text{mixer}} = IP3_{\text{CE's } V_{be} \rightarrow I}$



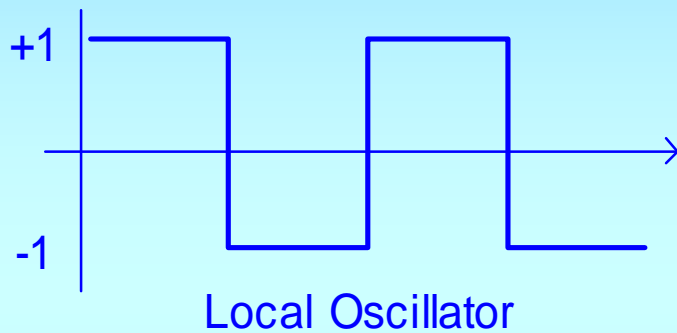
$$I_{Q_B} = \alpha I_s e^{(V_{BB} + v_{in})/v_t} = I_{DC} \left(1 + \frac{1}{v_t} v_{in} + \frac{1}{2v_t^2} v_{in}^2 + \frac{1}{6v_t^3} v_{in}^3 + \dots \right)$$

Double Balanced Mixer

- Basically two CE mixers
 - One gets $+v_{in}/2$, the other gets $-v_{in}/2$

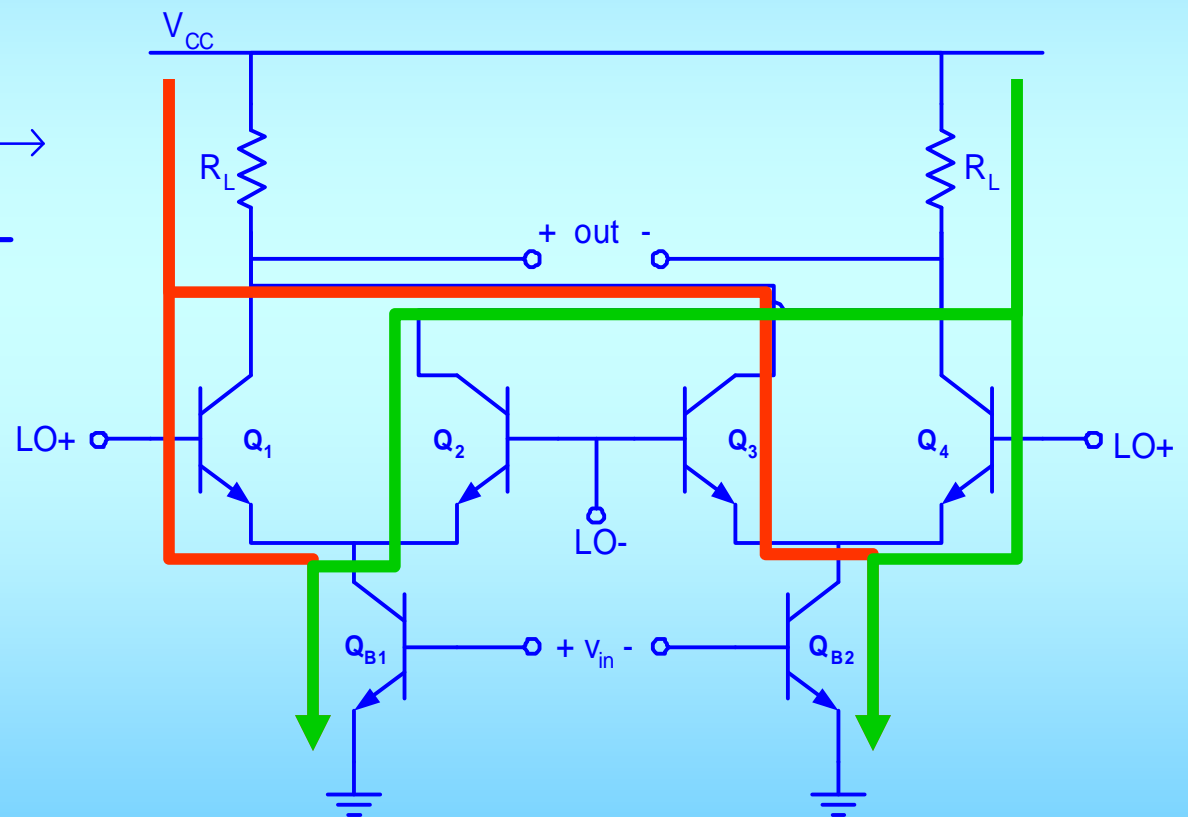


Double Balanced Mixer



$$V_{out} = g_m V_{in} R_L$$

$$V_{out} = -g_m V_{in} R_L$$

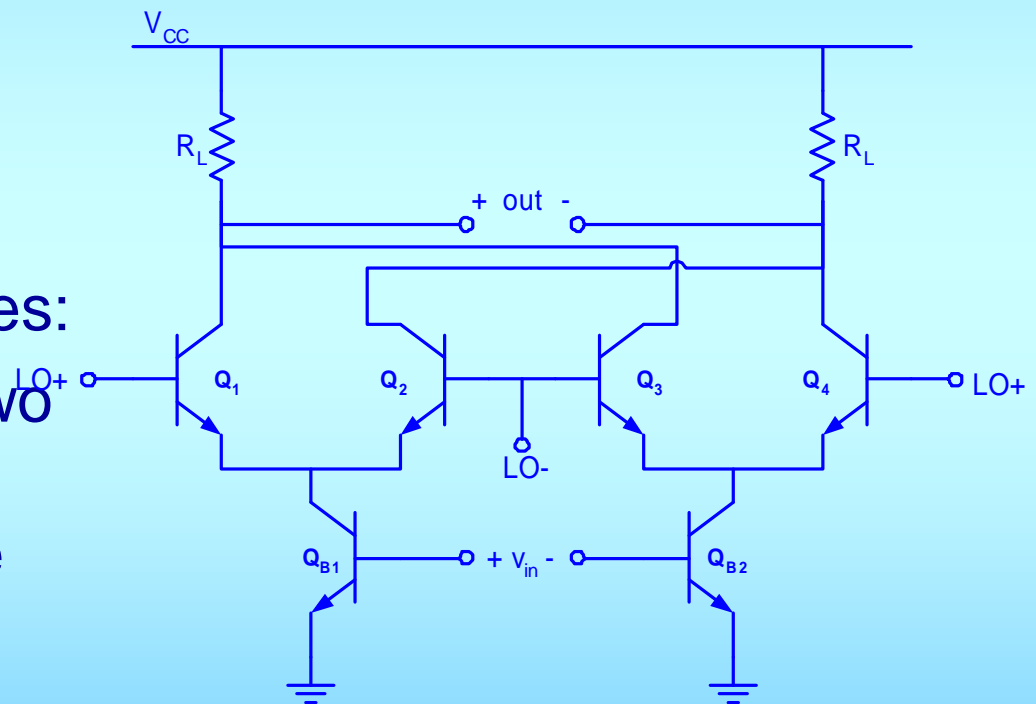


Double Balanced Mixer

- Benefits:
 - Fully Differential
 - No output signal at ω_{LO}
- Three stages:
 - CE input stages
 - Switches
 - Output load

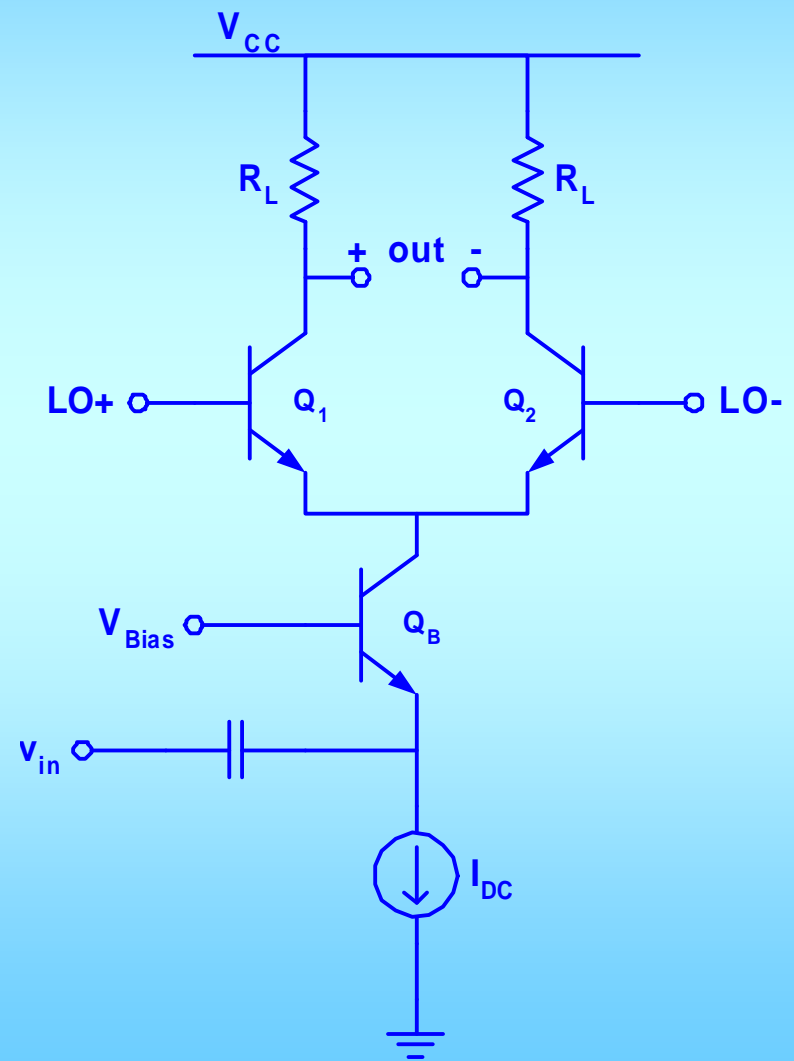
Double Balanced Mixer

- Noise:
 - Similar to CE Mixer
- IP3:
 - Expansion of differential gain gives:
 - V_{in} split between two Q's, it can double before reaching the same level of nonlinearity
 - IIP3 improved by 3 dB



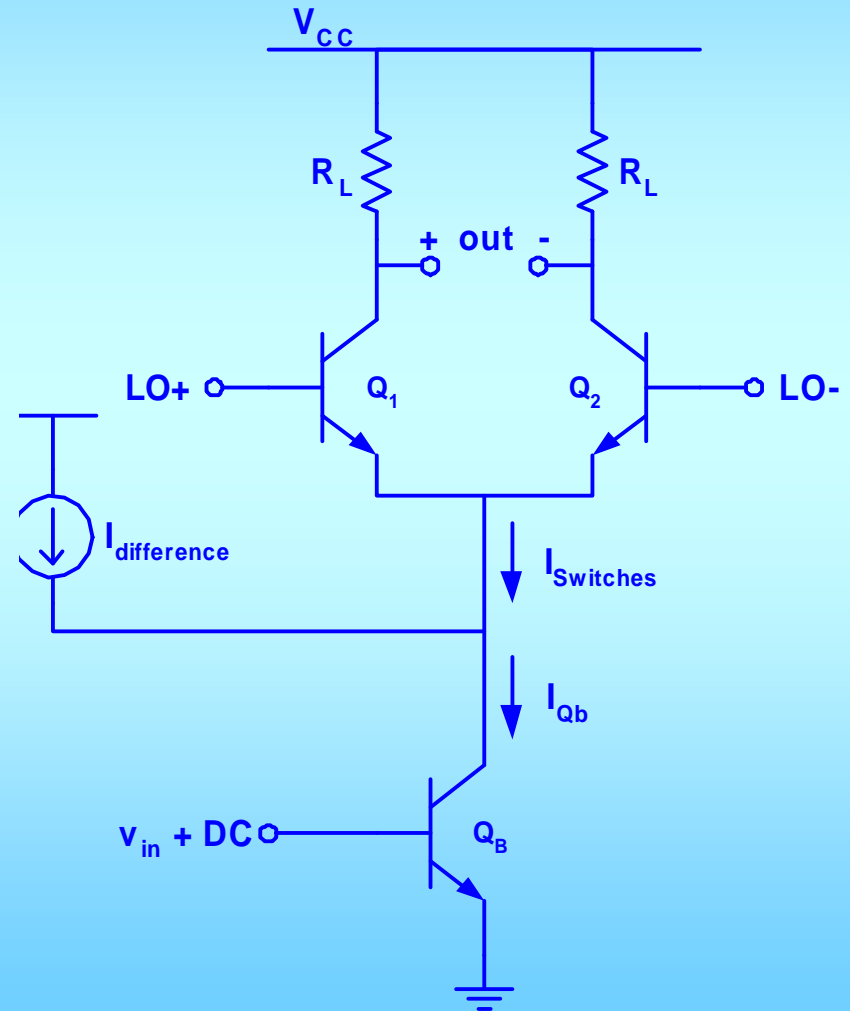
Common Base Mixers

- Similar operation to CE mixers
- Different input stage
 - Q_B is CB
- Slightly different output noise
 - Different CB input noise
- Better linearity



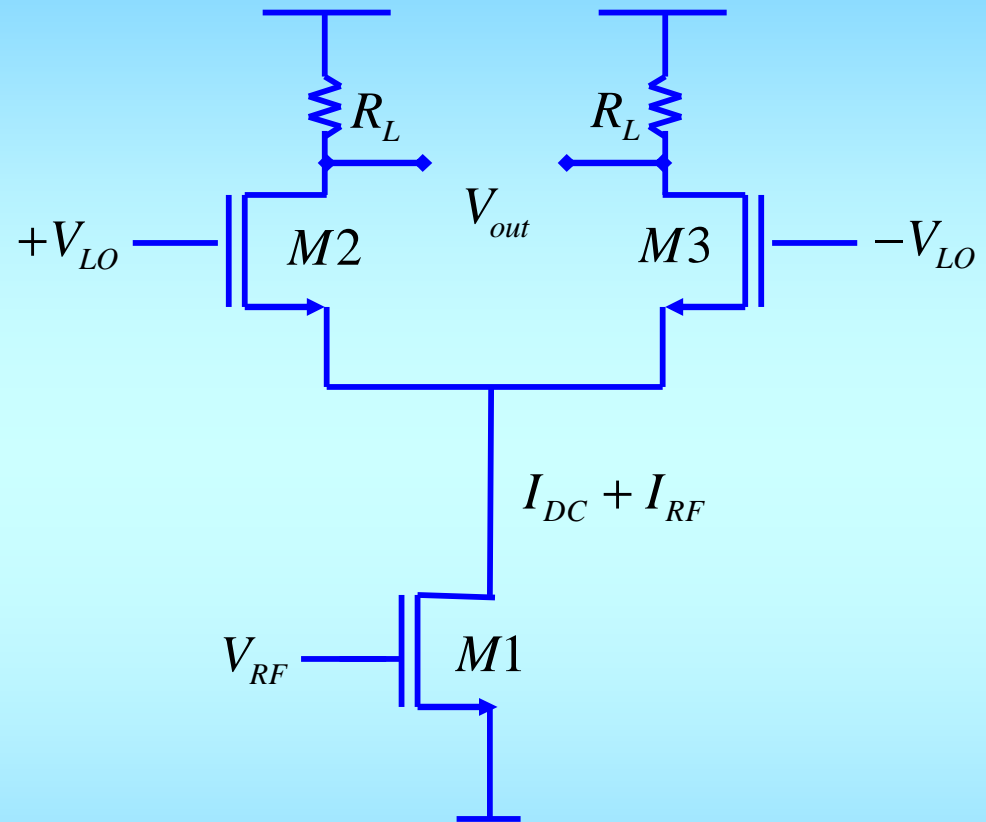
Mixer Improvements

- Debiasing switches from input transistors:
 - To lower NF we want high g_m , but low Q_1 and Q_2 current
 - **Conflicting!**
 - We can set low I_{Switches} and high I_{Q_b} using a current source

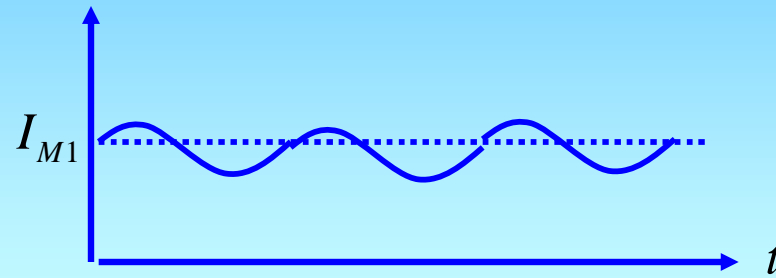


MOS Single Balanced Mixer

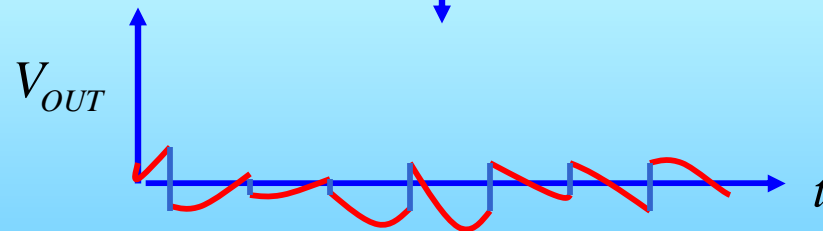
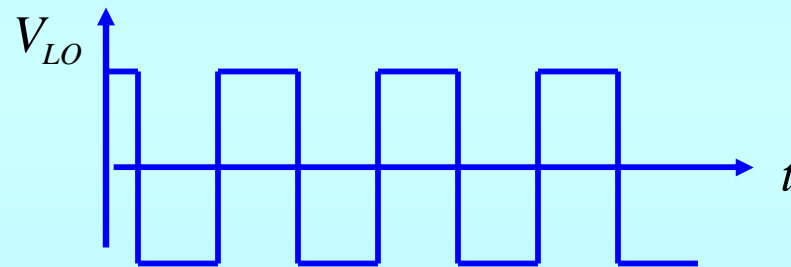
- The transistor M1 converts the RF voltage signal to the current signal.
- Transistors M2 and M3 commute the current between the two branches.



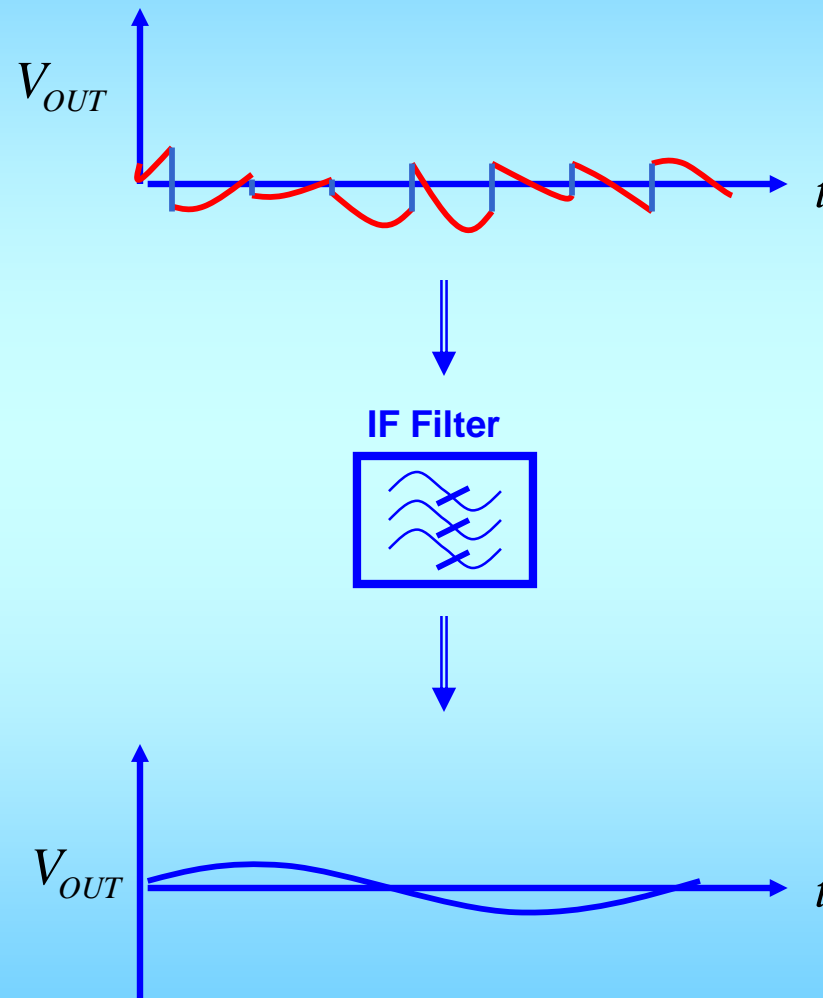
MOS Single Balanced Mixer



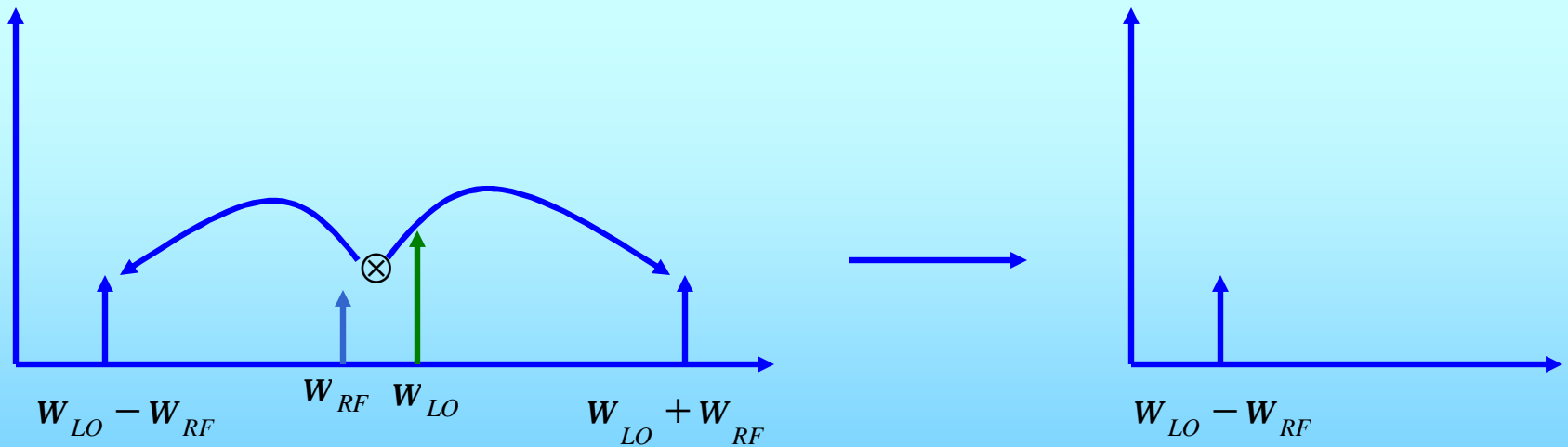
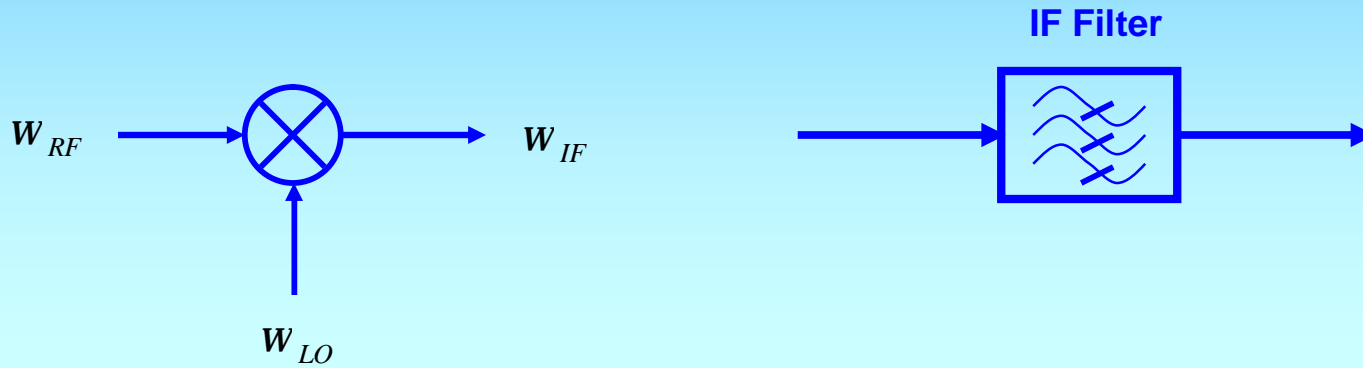
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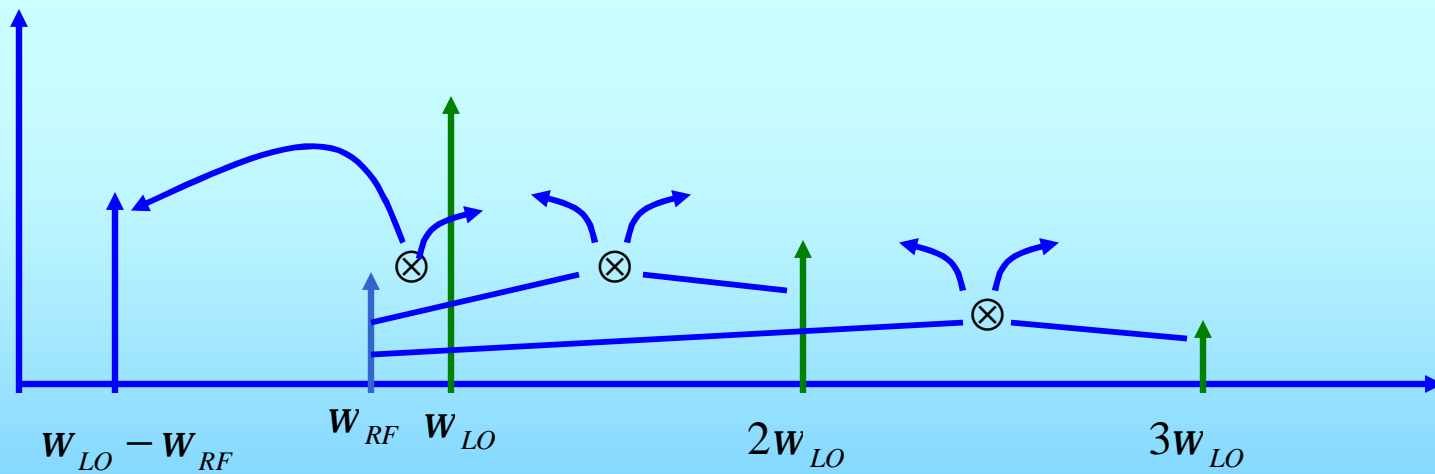
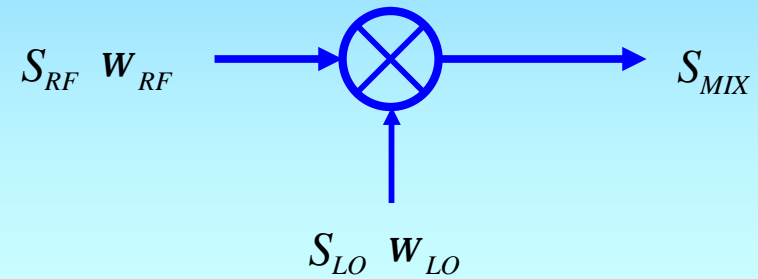
MOS Single Balanced Mixer



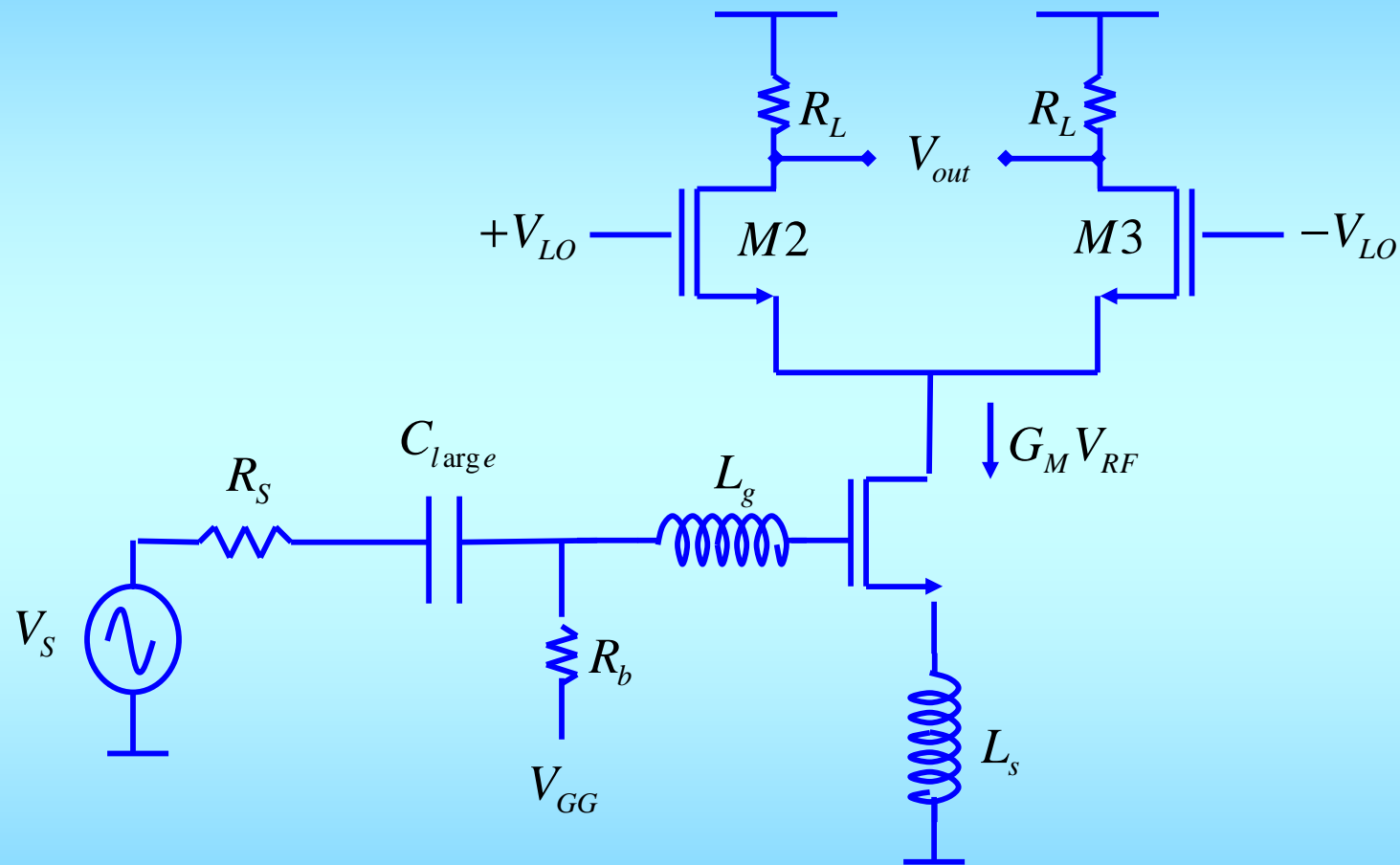
MOS Single Balanced Mixer



MOS Single Balanced Mixer

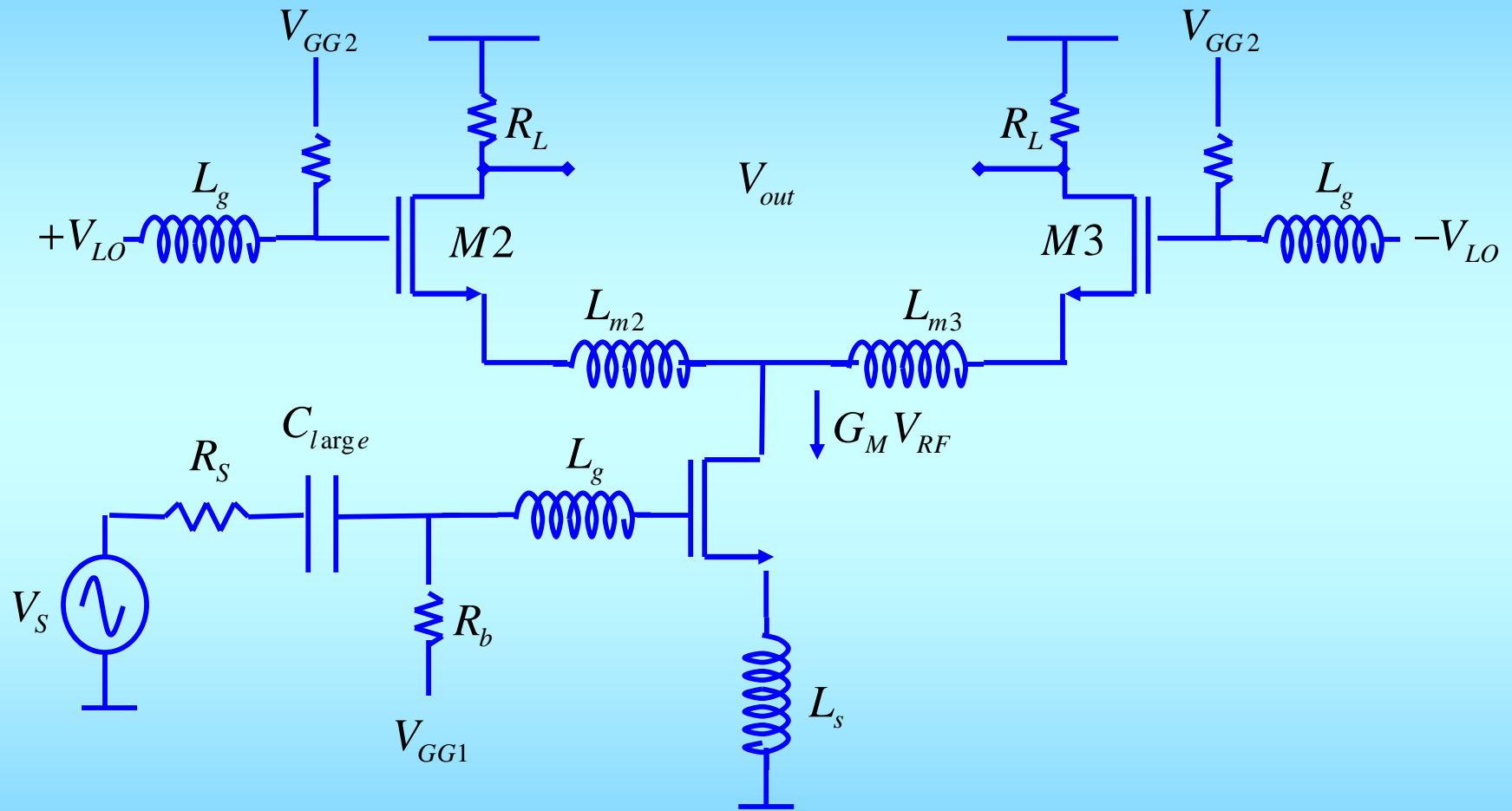


Single Balanced Mixer Analysis (Incl. Impd. Match)



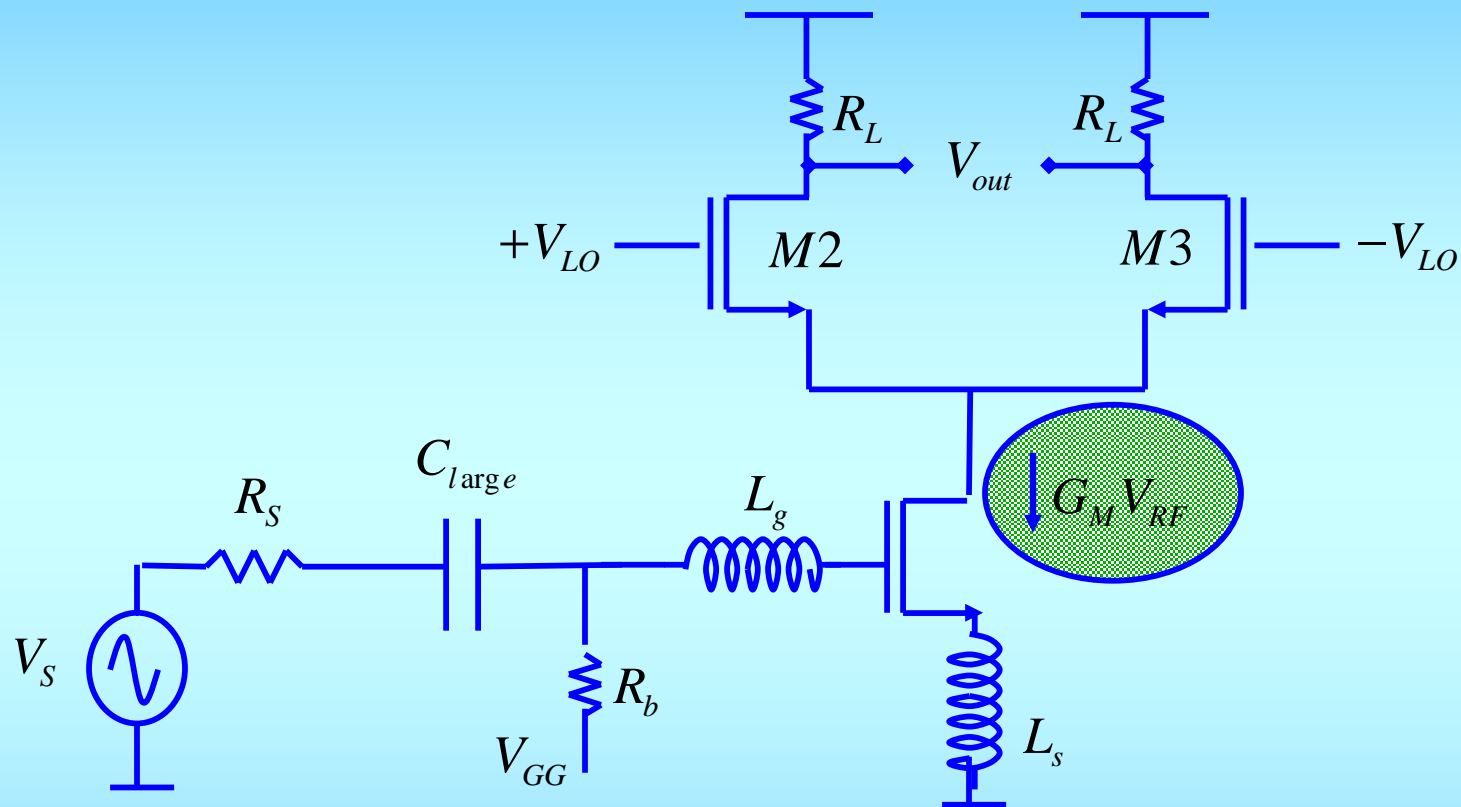
This architecture, without impedance matching for the LO port, is very commonly used in many designs.

Single Balanced Mixer Analysis (Incl. Impd. Match)



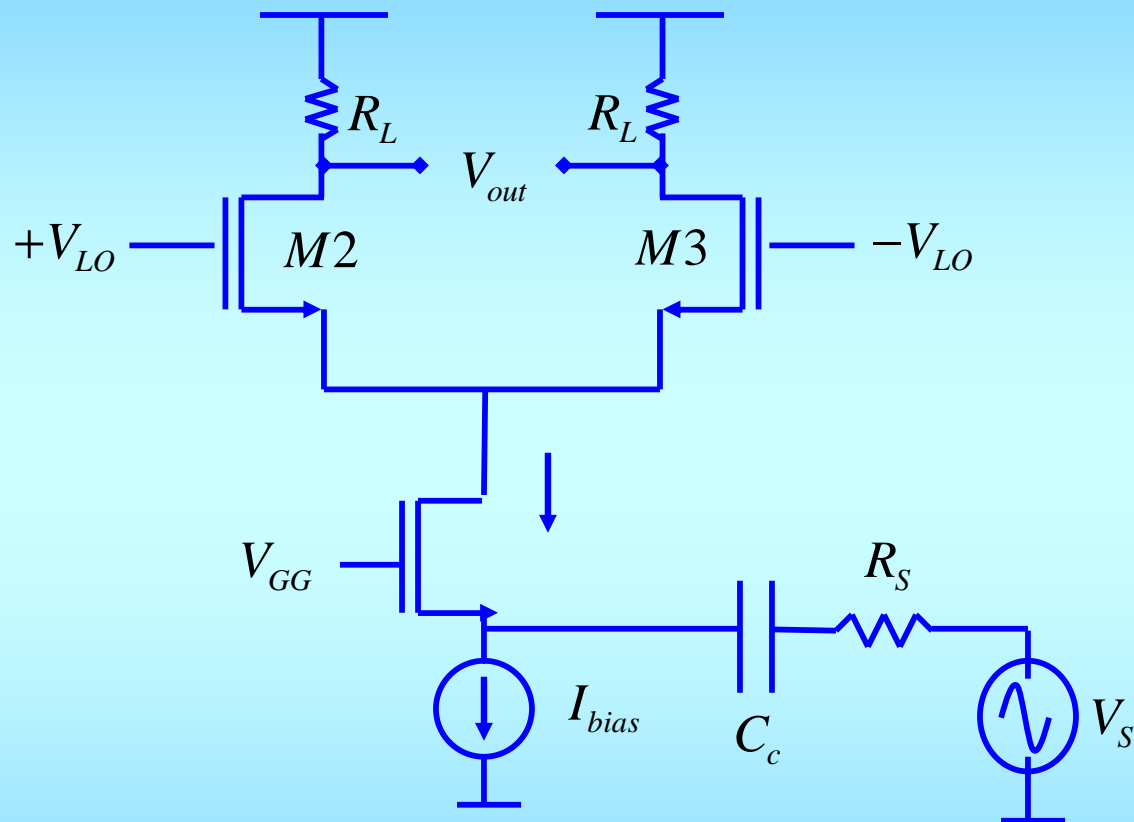
- This architecture, with impedance matching for the LO port, maximizes LO power utilization without wasting it.

Single Balanced Mixer Analysis: Linearity



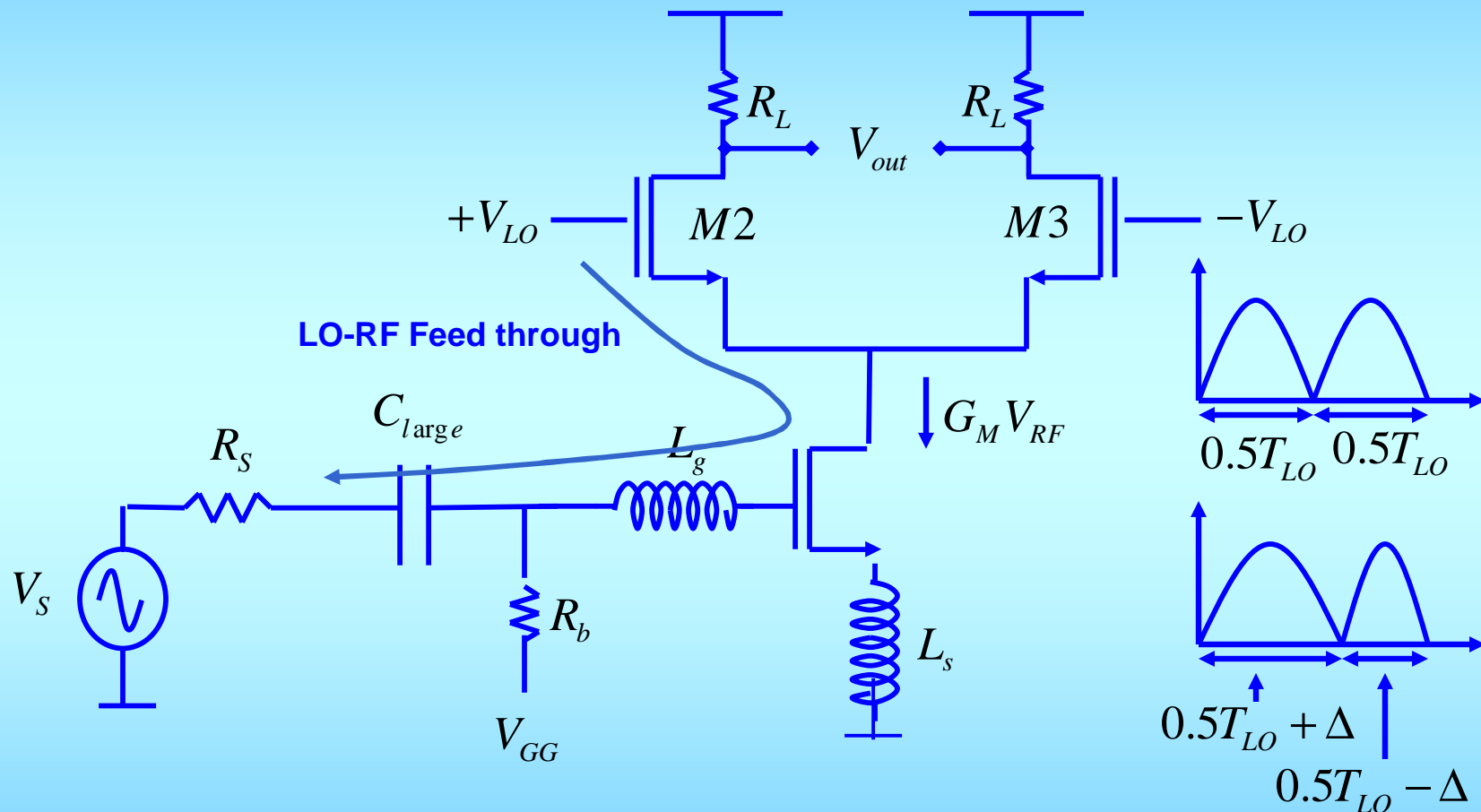
- Linearity of the Mixer primarily depends on the linearity of the transducer ($I_{tail} = G_m \cdot V_{rf}$). Inductor L_s helps improve linearity of the transducer.
- The transducer transistor M1 can be biased in the linear law region to improve the linearity of the Mixer. Unfortunately this results in increasing the noise figure of the mixer (as discussed in LNA design).

Single Balanced Mixer Analysis: Linearity



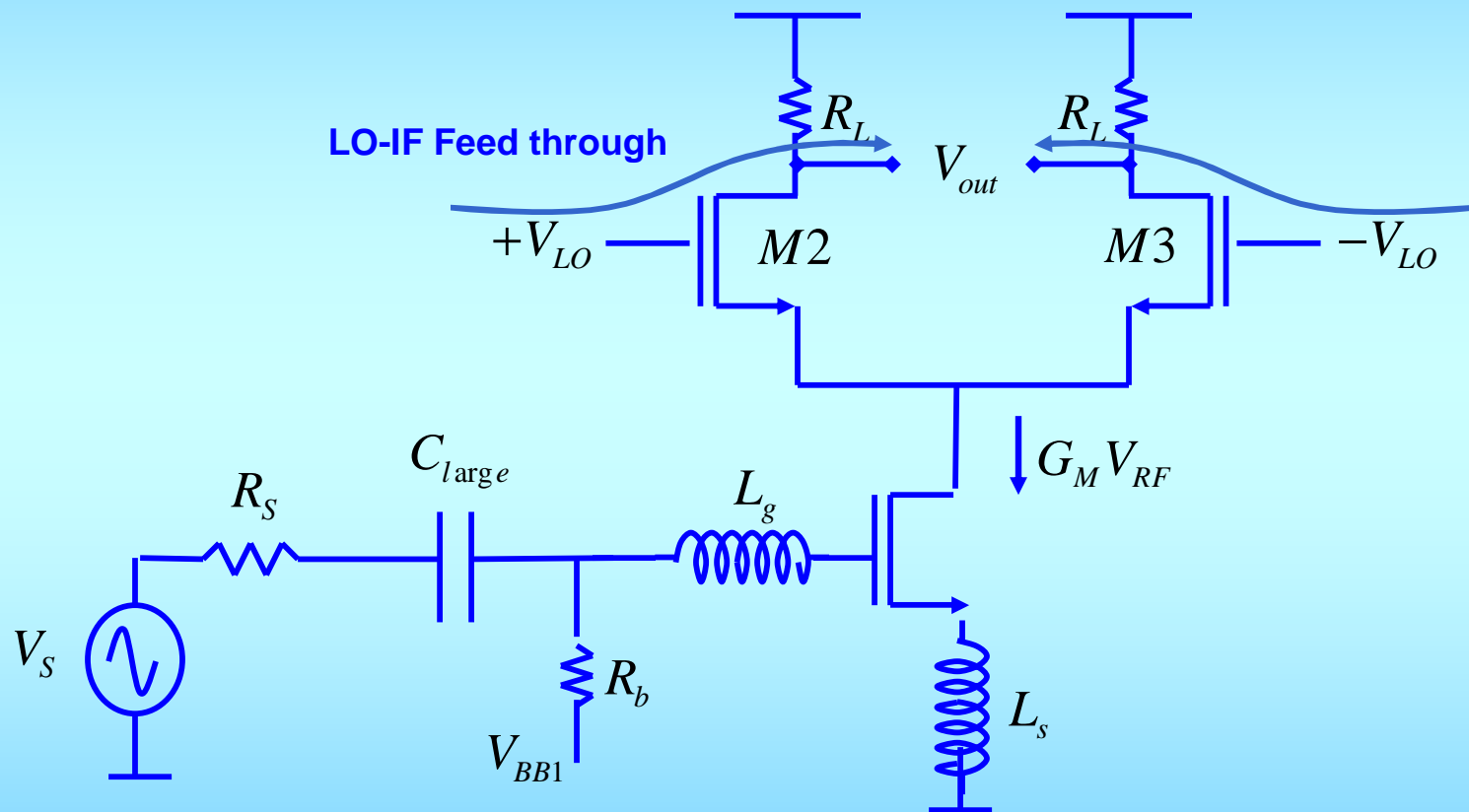
- Using the common gate or common base stage as the transducer improves the linearity of the mixer. Unfortunately the approach reduces the gain and increases the noise figure of the mixer.

Single Balanced Mixer Analysis: Isolation



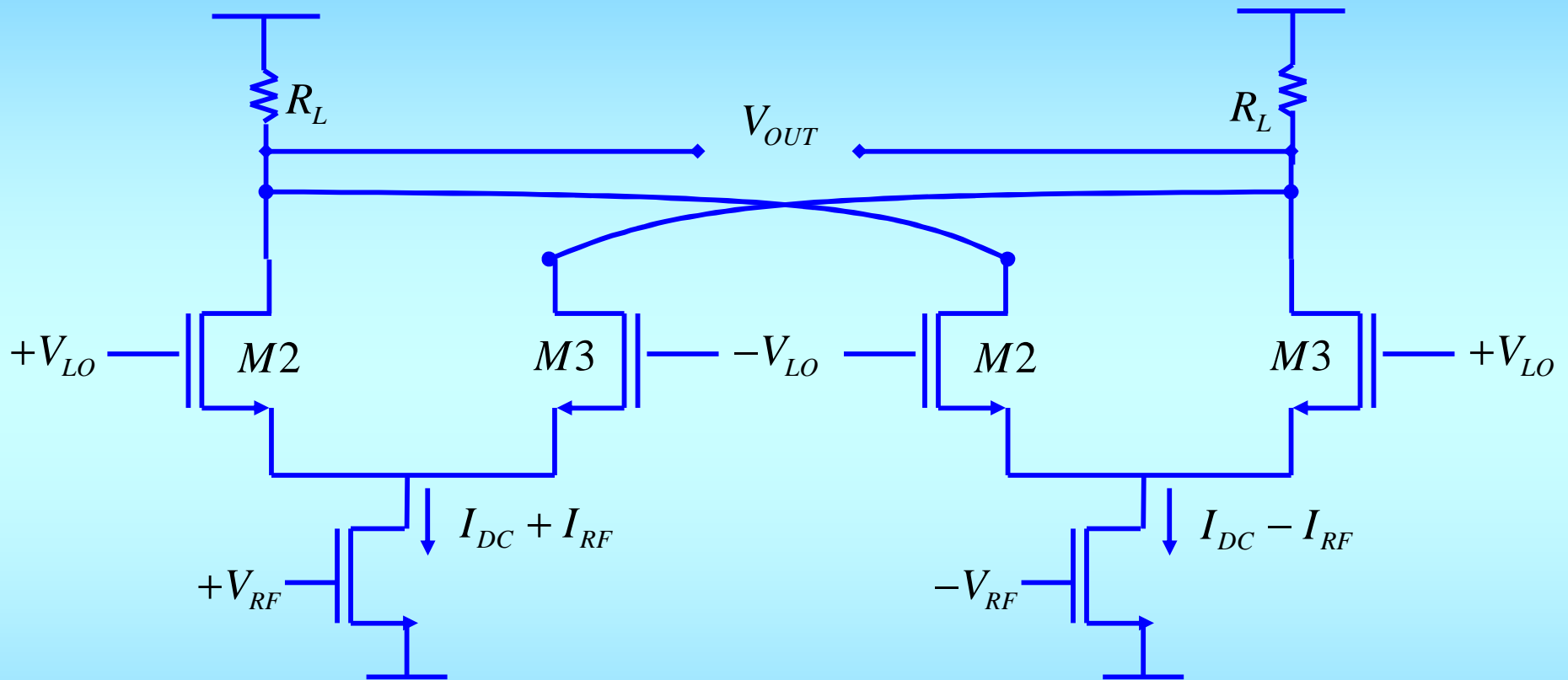
- The strong LO easily feeds through and ends up at the RF port in the above architecture especially if the LO does not have a 50% duty cycle. Why?

Single Balanced Mixer Analysis: Isolation



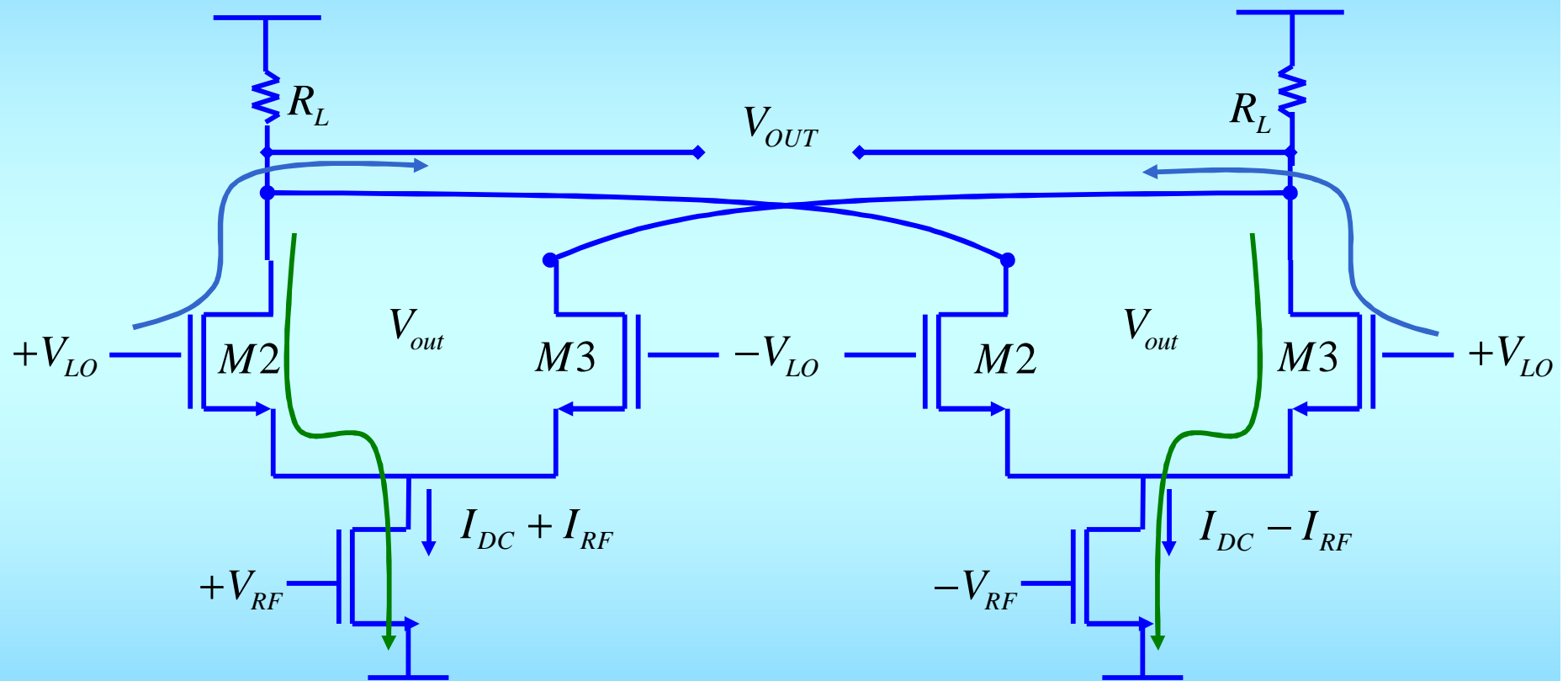
- The strong LO-IF feed-through may cause the mixer or the amplifier following the mixer to saturate. It is therefore important to minimize the LO-IF feed-through.

Double Balanced Mixer



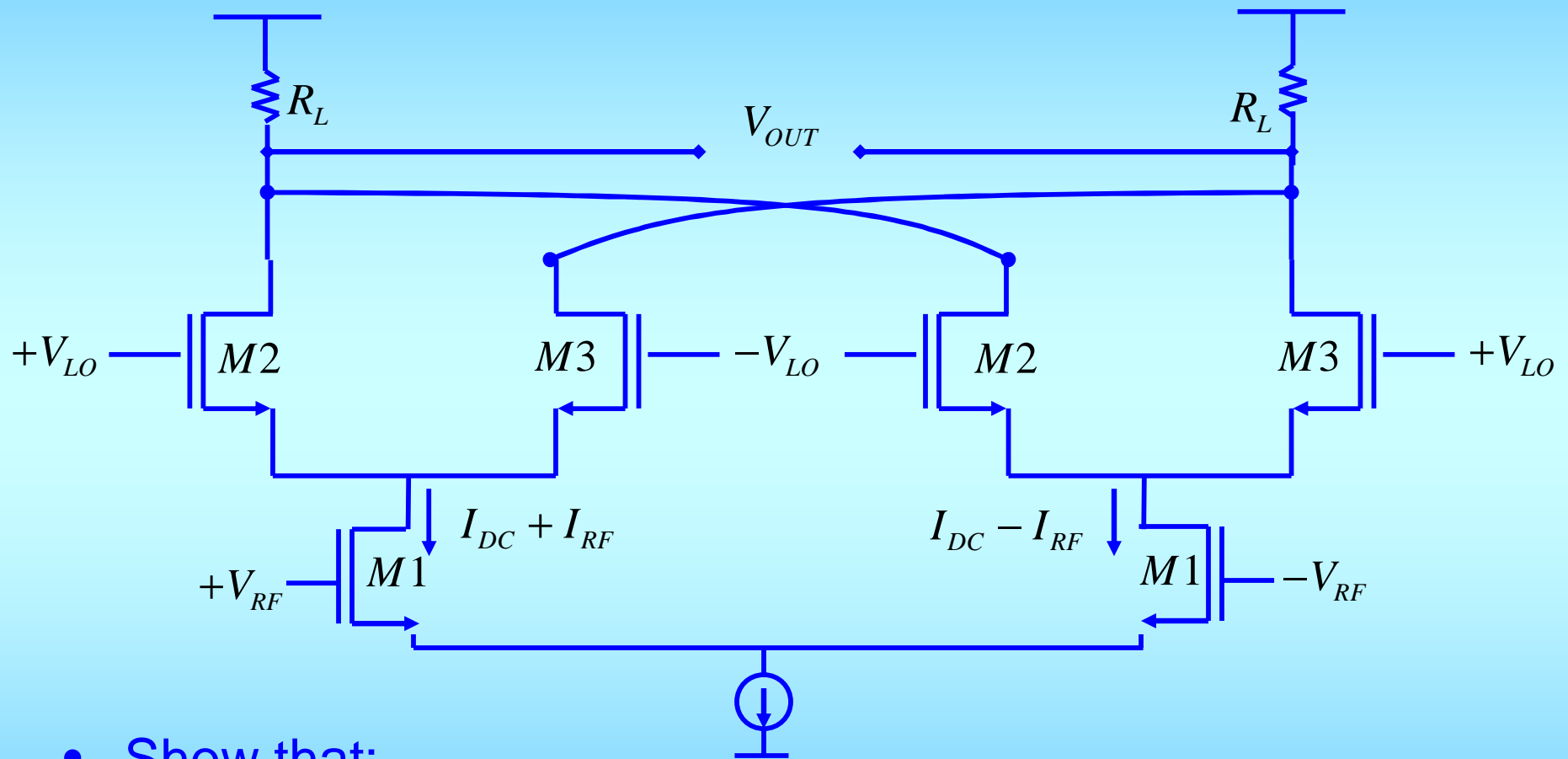
- Strong LO-IF feed suppressed by double balanced mixer.
- All the even harmonics cancelled.
- All the odd harmonics doubled (including the signal).

Double Balanced Mixer



- The LO feed through cancels.
- The output voltage due to RF signal doubles.

Double Balanced Mixer: Linearity

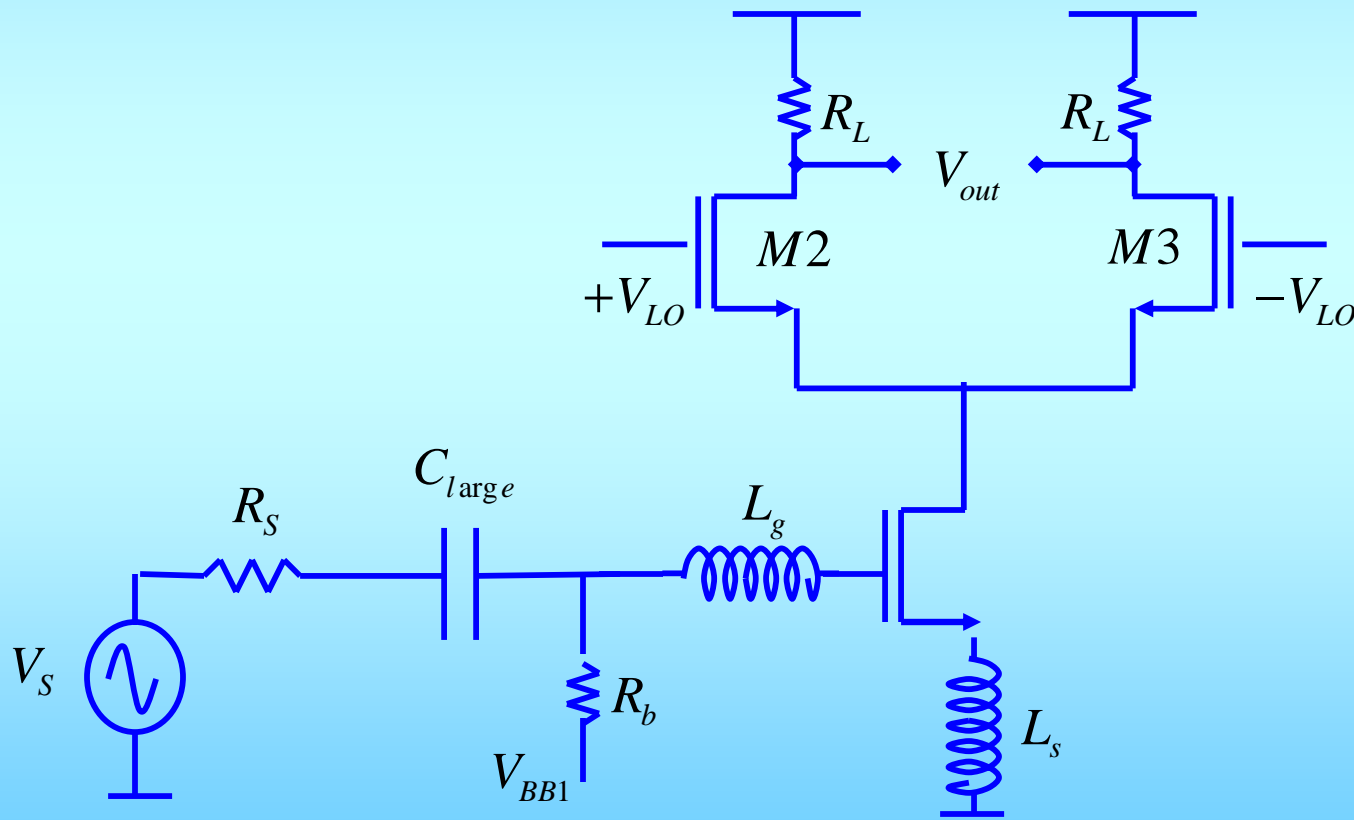


- Show that:

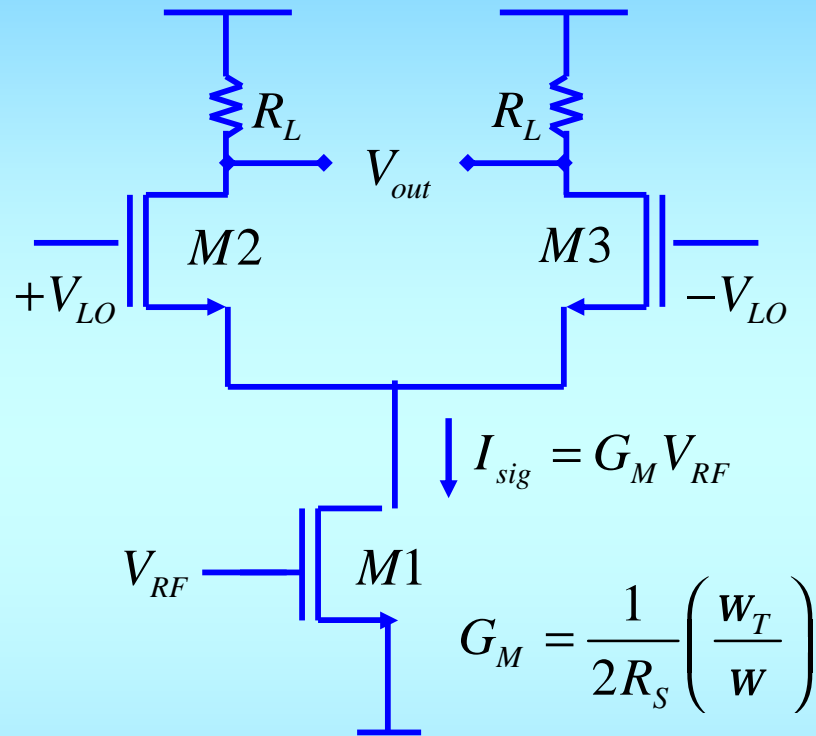
$$V_{IF} = 2I_{DC}R_L \left\{ \left(\frac{K_{SQ}}{2I_{DC}} \right)^{1/2} * V_{RF} + \frac{1}{2} \cdot \left(\frac{K_{SQ}}{2I_{DC}} \right)^{3/2} V_{RF}^3 + \dots \right\} \quad IIP_3 \text{ in - volts} = \sqrt{\frac{8I_{DC}}{3K_{SQ}}}$$

Mixer Input Match

$$R_S = R_g + w_T L_S \quad w(L_g + L_s) = \frac{1}{wC_{gs}}$$



Mixer Gain



$$0 \rightarrow \frac{T_{LO}}{2} : V_{out} = [V_{cc} - (I_{DC} + I_{sig}) \cdot R_L] - [V_{cc}] = -(I_{DC} + I_{sig}) \cdot R_L$$

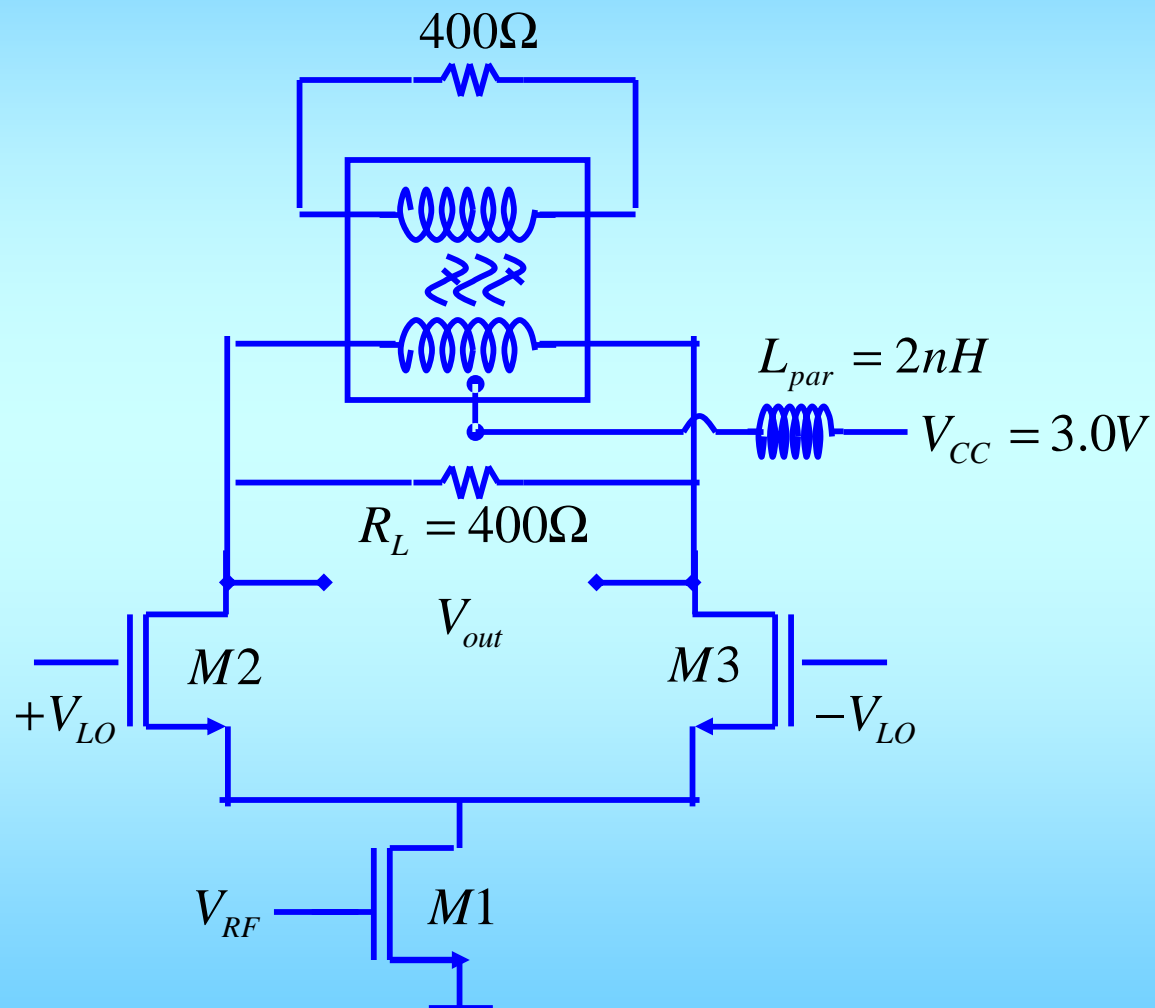
$$\frac{T_{LO}}{2} \rightarrow T_{LO} : V_{out} = [V_{cc}] - [V_{cc} - (I_{DC} + I_{sig}) \cdot R_L] = (I_{DC} + I_{sig}) \cdot R_L$$

$$V_{out-sig} = I_{sig} R_L * SW = I_{sig} R_L \frac{4}{p} \left(\cos w_{LO} t - \frac{1}{3} \cos 3w_{LO} t + \frac{1}{5} \cos 5w_{LO} t - \frac{1}{7} \cos 7w_{LO} t + \mathbf{L} \right)$$

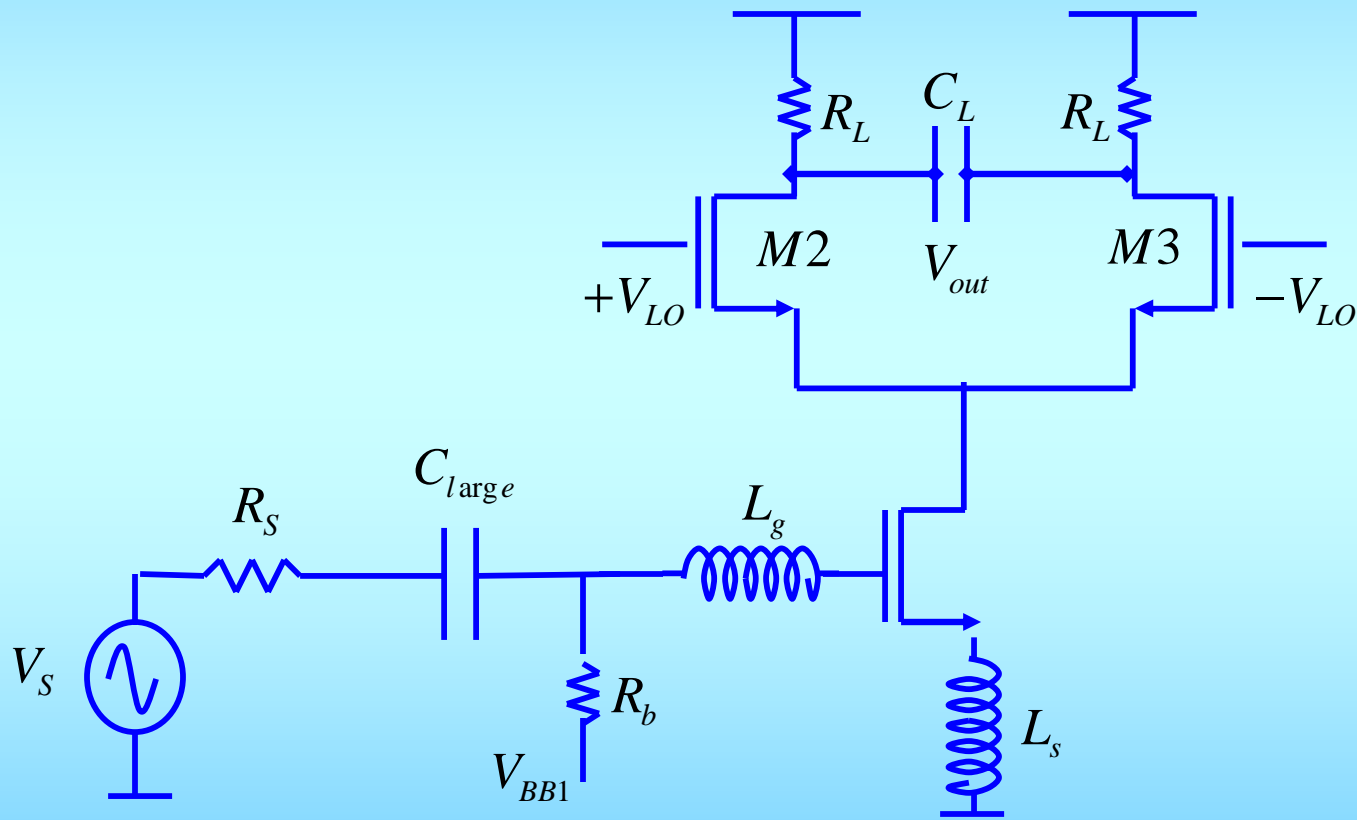
Mixer Output Match

- Heterodyne Mixer: For IF frequencies of 100-200MHz (signal bandwidth of 4MHz), no impedance matching due to:
 - The signal bandwidth is comparable to the IF frequency therefore the impedance matching would create gain and phase distortions
 - Need large inductors and capacitors to impedance match at 200MHz

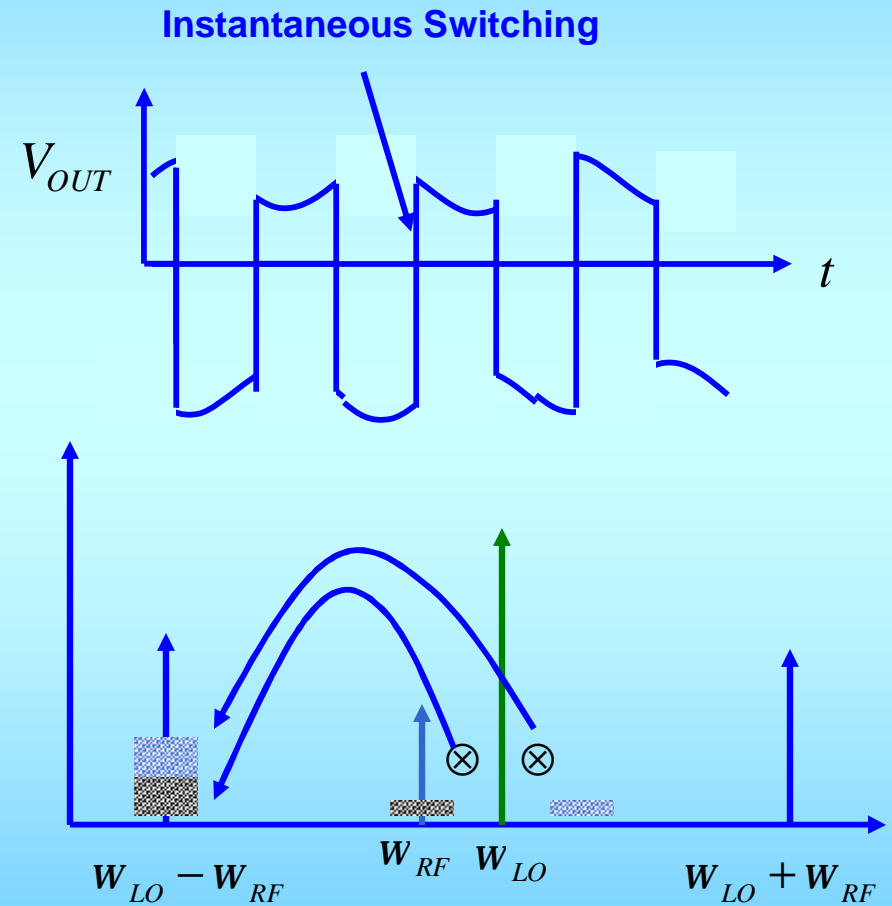
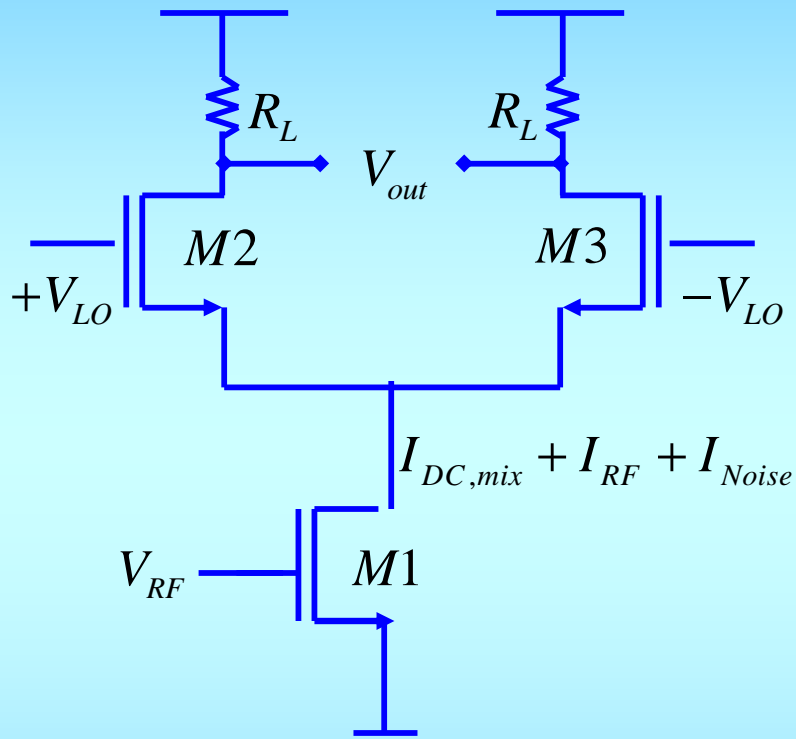
Mixer Output Match (IF)



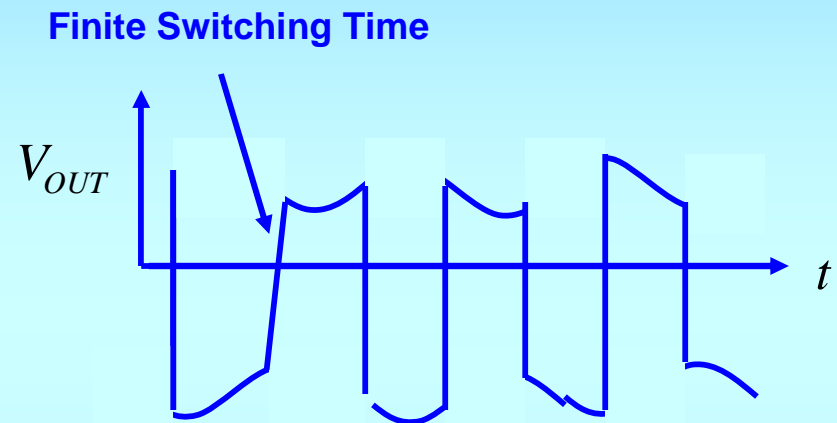
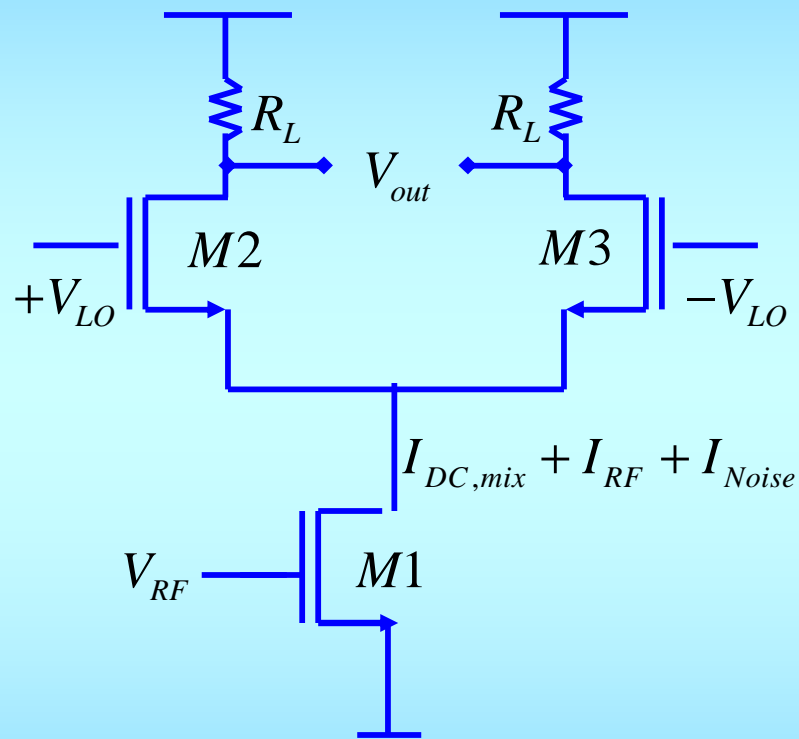
Mixer Output Match (direct conversion)



Mixer Noise Analysis



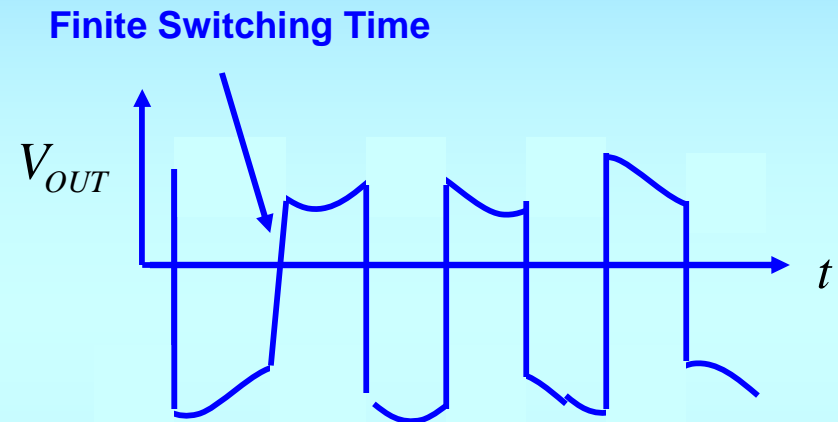
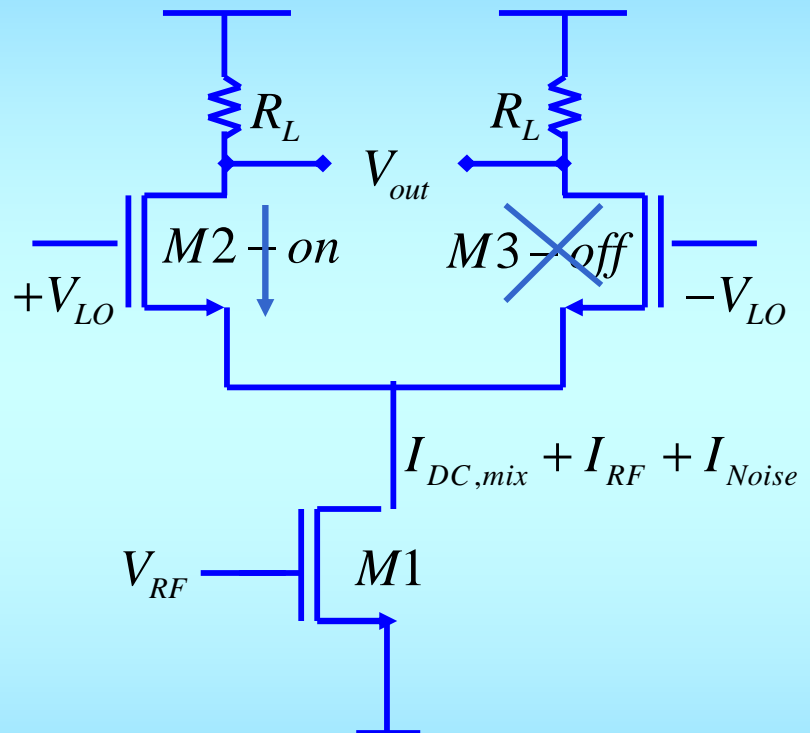
Mixer Noise Analysis



- If the switching is not instantaneous, additional noise from the switching pair will be added to the mixer output.
- Let us examine this in more detail.

Mixer Noise Analysis

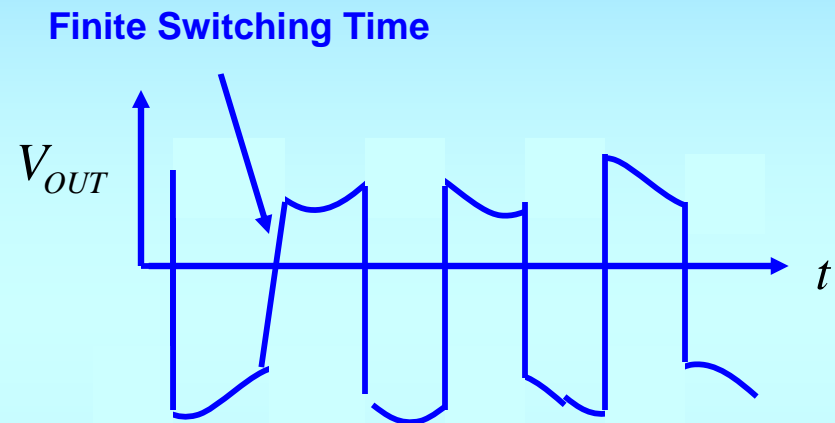
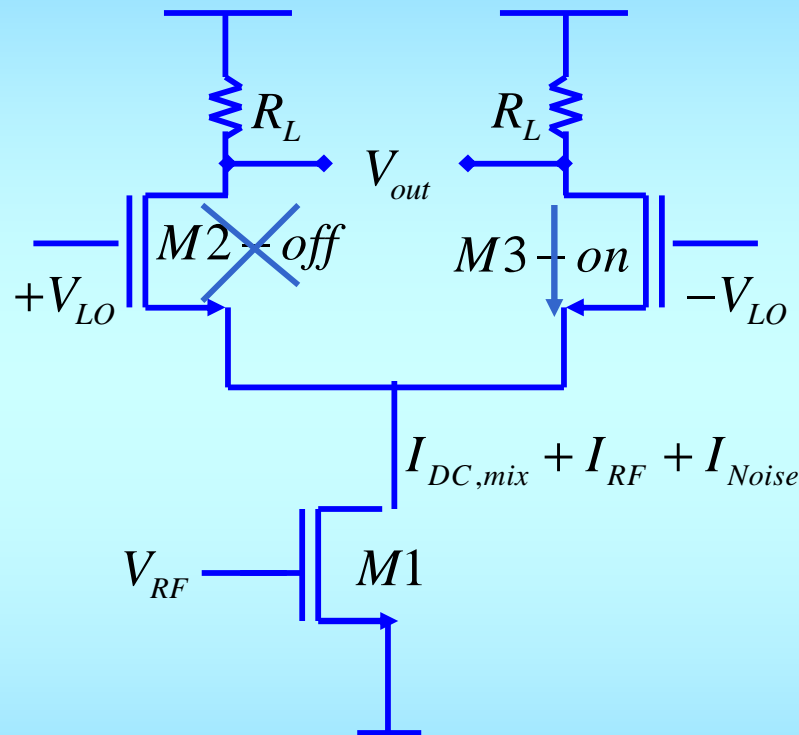
- Noise analysis of a single balanced mixer cont....:



- When M2 is on and M3 is off:
 - M2 does not contribute any additional noise (M2 acts as cascode)
 - M3 does not contribute any additional noise (M3 is off)

Mixer Noise Analysis

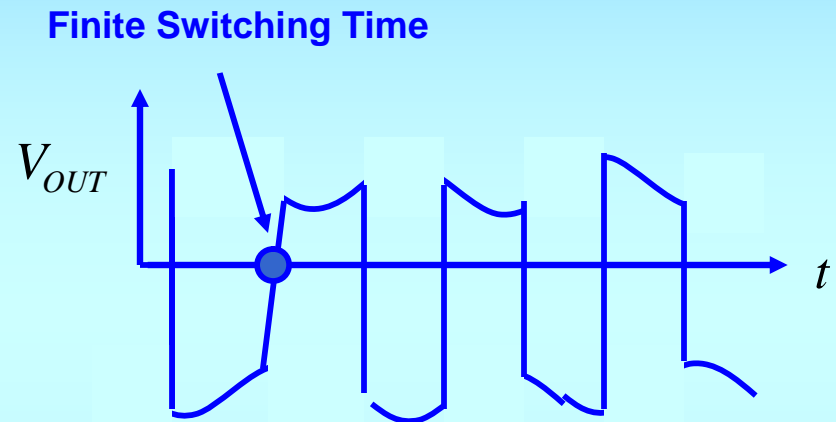
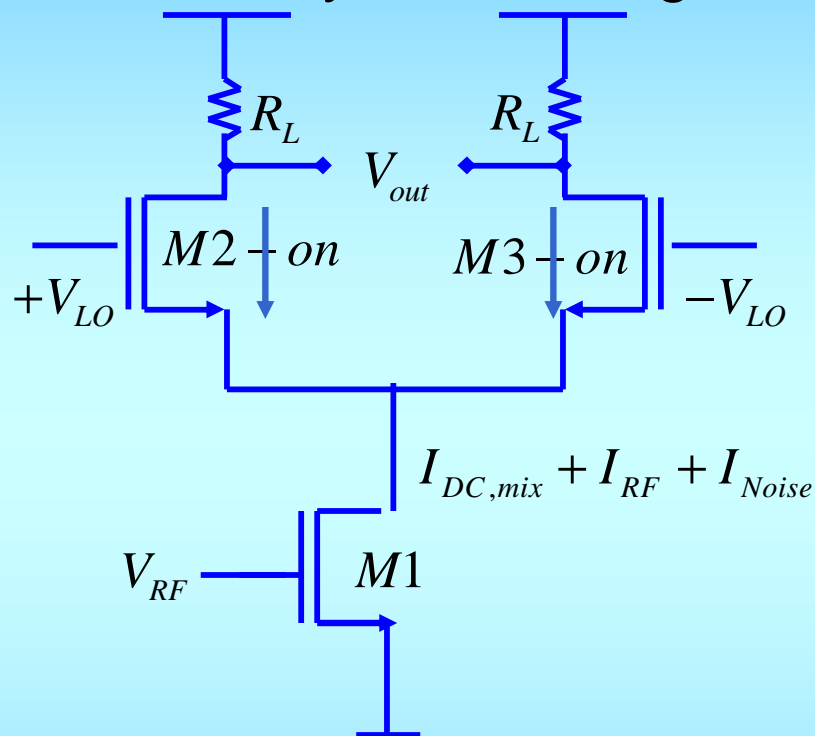
- Noise analysis of a single balanced mixer cont...:



- When M2 is off and M3 is on:
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Mixer Noise Analysis

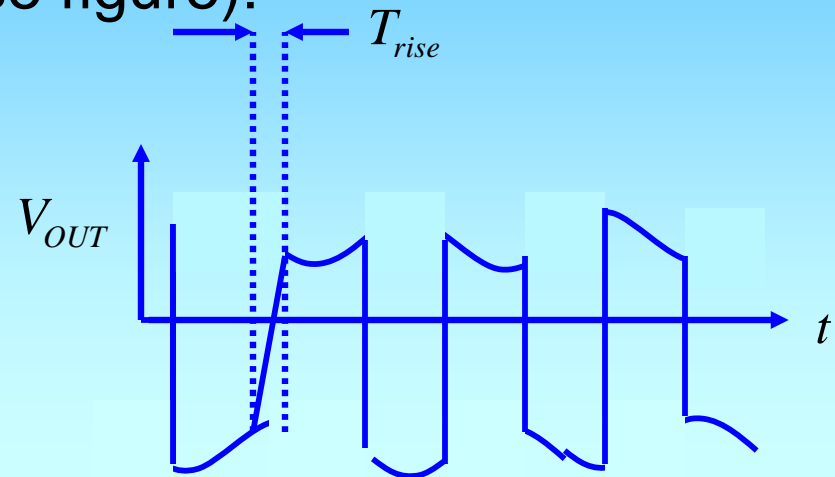
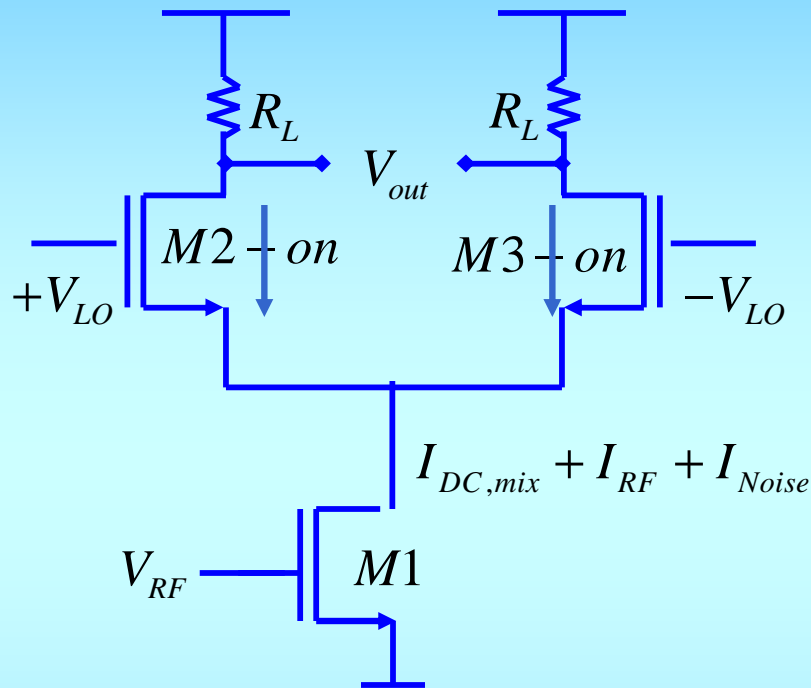
- Noise analysis of a single balanced mixer cont....:



- When $V_{LO+} = V_{LO-}$ (i.e. the LO is passing through zero), the noise contribution from the transducer (M1) is zero. Why?
- However, the noise contributed from M2 and M3 is not zero because both transistors are conducting and the noise in M2 and M3 are uncorrelated.

Mixer Noise Analysis

- Optimizing the mixer (for noise figure):



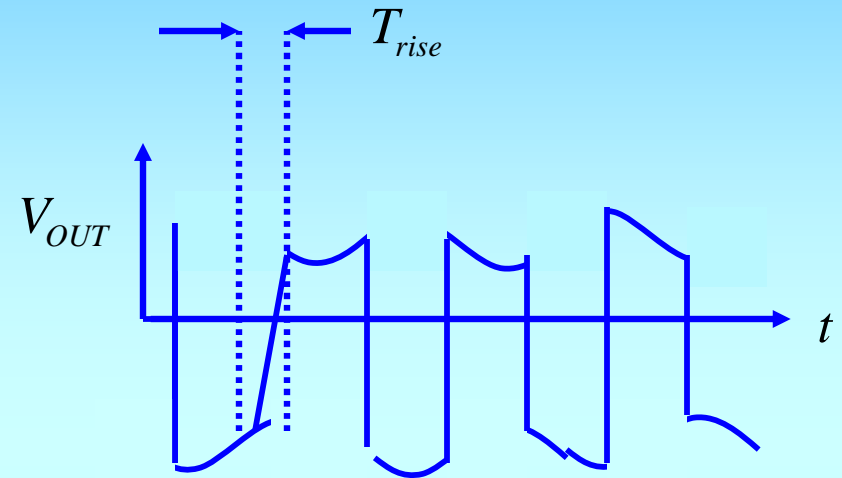
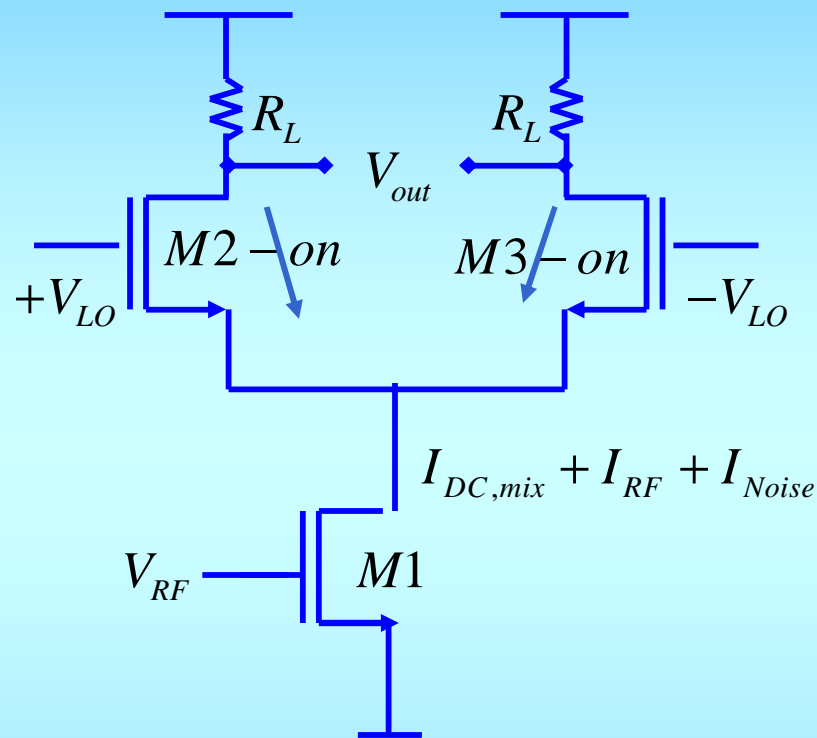
$$g_m \propto \sqrt{W} \dots \text{fixed} - I_{DC}$$

$$W_T \propto \frac{1}{\sqrt{W}} \dots \text{fixed} - I_{DC}$$

- Design the transducer for minimum noise figure.
- Noise from M2, M3 minimized by fast switching :
 - making LO amplitude large
 - making M2 and M3 short (i.e. increasing f_T of M2 and M3)
- Noise from M2, M3 can be minimized by using wide M2/M3 switches.

Mixer Noise Analysis

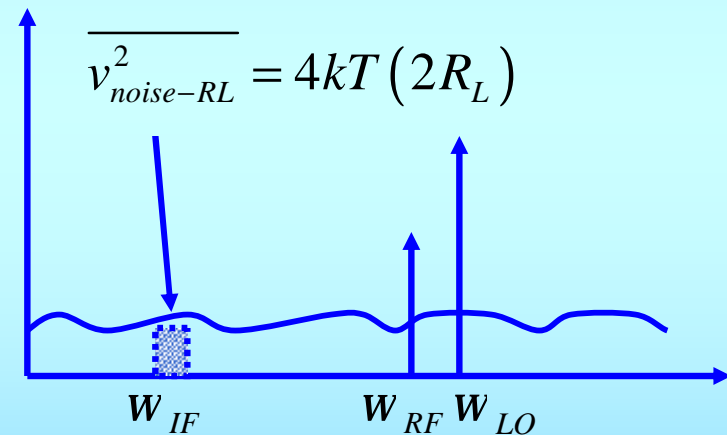
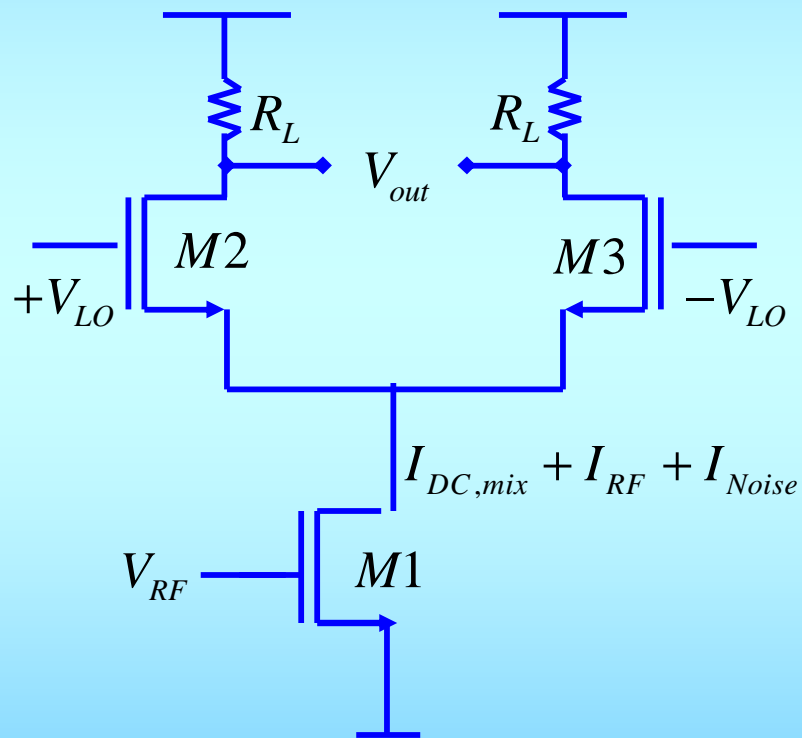
- Noise Figure Calculation:



- Let us calculate the noise figure including the contribution of $M2/M3$ during the switching process.

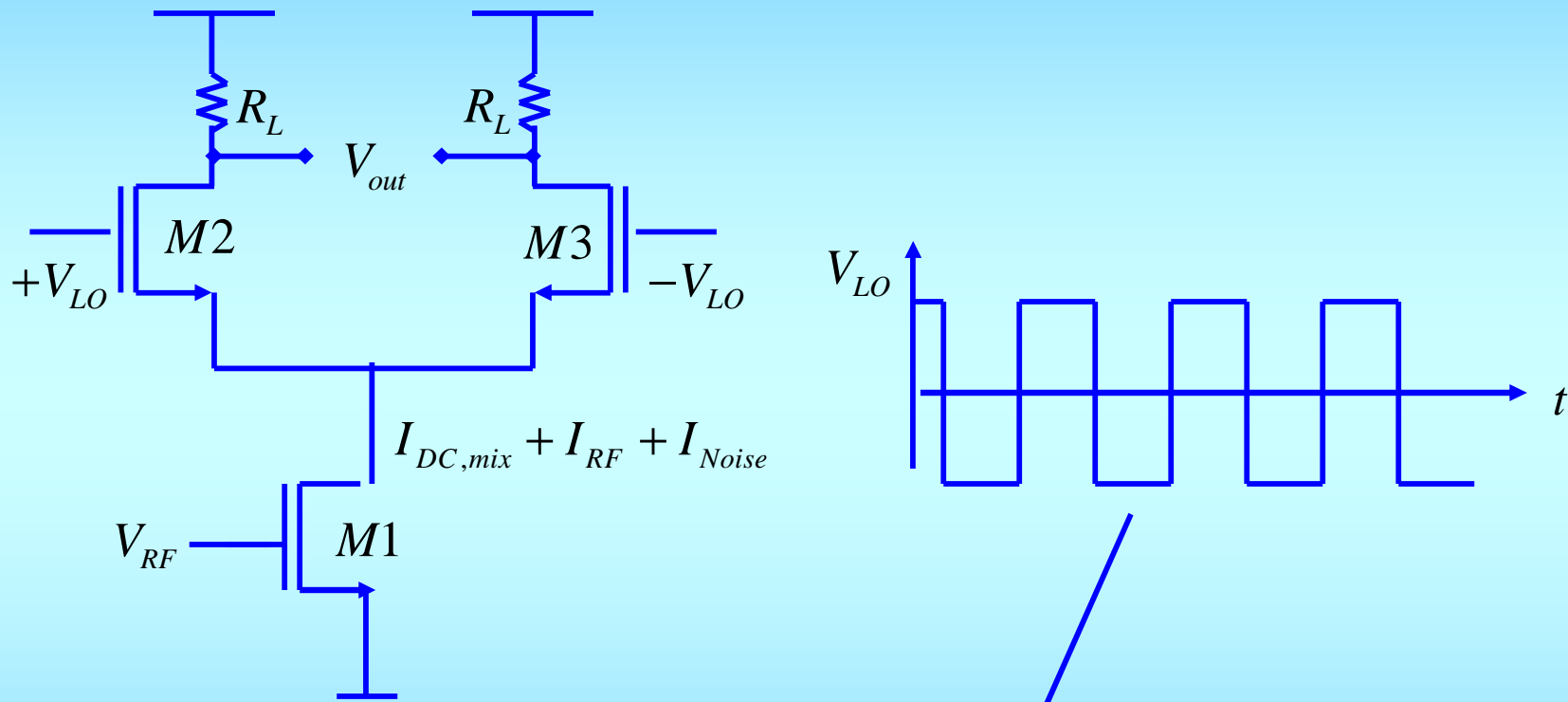
Mixer Noise Analysis: RL Noise

- Noise Analysis of Heterodyne Mixer (RL noise):



Mixer Noise Analysis: Transducer Noise

- Noise Analysis of Heterodyne Mixer (Transducer noise):



$$i_{noise-M1-switch} = i_{noise-M1}(t) \cdot SW(t)$$

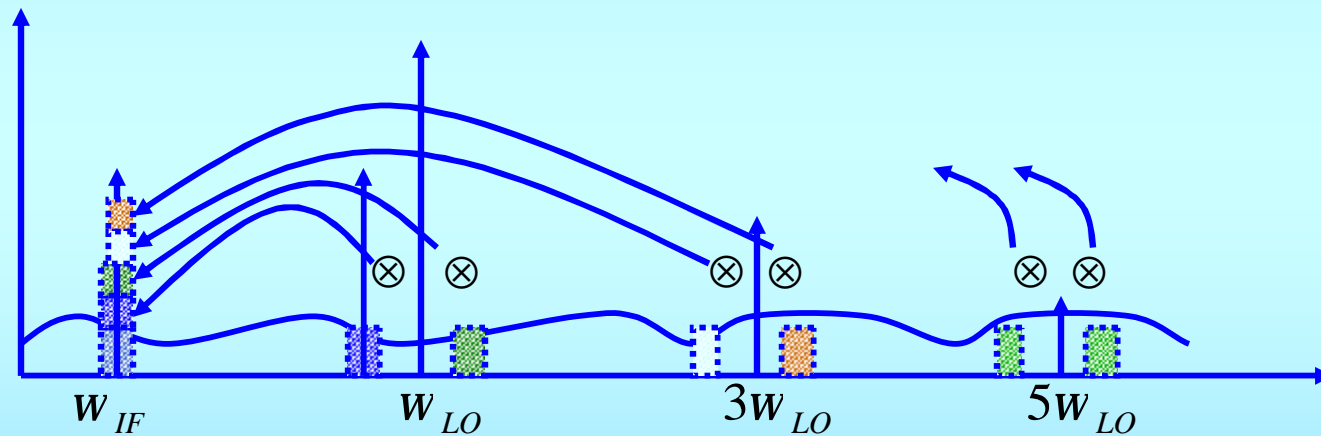
$$= i_{noise-M1}(t) \cdot \left(\frac{4}{p} \text{Cos}\{w_{LO}t\} - \frac{4}{3p} \text{Cos}\{3w_{LO}t\} + \frac{4}{5p} \text{Cos}\{5w_{LO}t\} - \dots \right)$$

Mixer Noise Analysis: Transducer Noise

- Noise Analysis of Heterodyne Mixer (Trans-conductor noise):

$$i_{noise-M1-switch} = i_{noise-M1}(t) \cdot SW(t)$$

$$= i_{noise-M1}(t) \cdot \left(\frac{4}{p} \cos\{w_{LO}t\} - \frac{4}{3p} \cos\{3w_{LO}t\} + \frac{4}{5p} \cos\{5w_{LO}t\} - \dots \right)$$



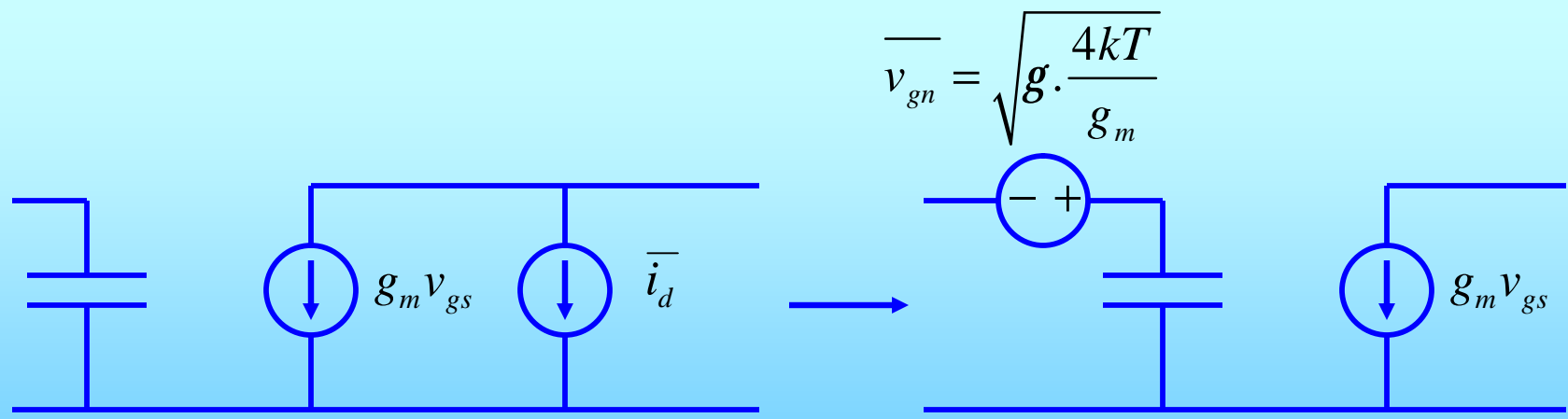
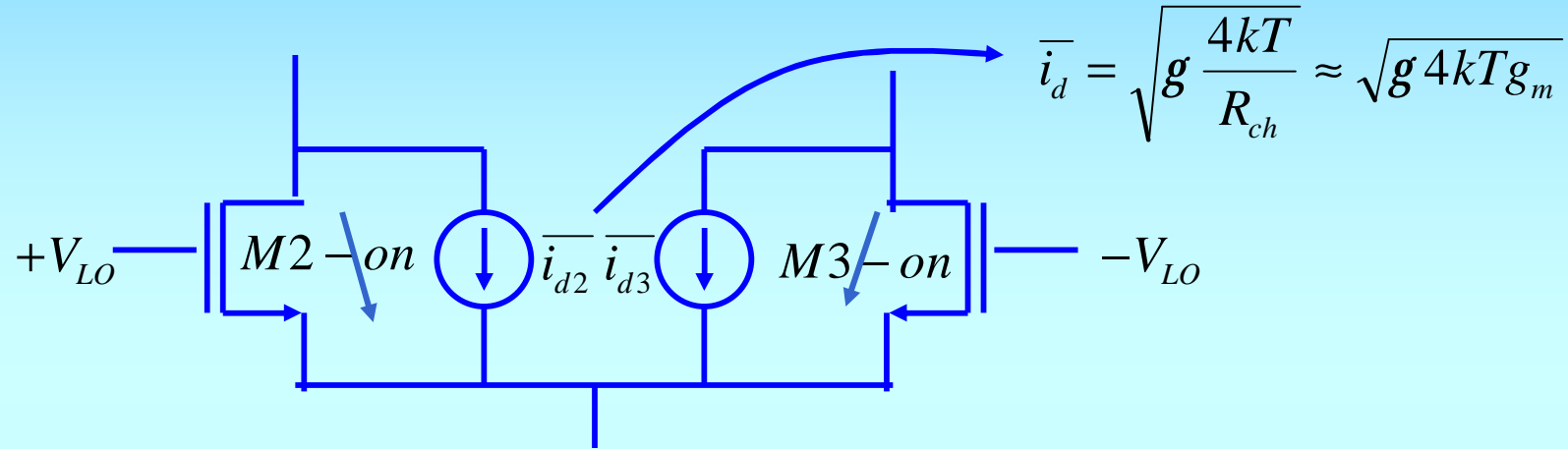
$$\overline{i_{noise-M1}^2}(f) = g \cdot \frac{4kT}{R_{ch}} = g \cdot 4kTg_{m1} \quad \overline{i_{noise-M1}^2}(w_{IF}) = 2 \cdot \left(\frac{4}{p} \right)^2 \cdot \left[1 + \frac{1}{3^2} + \frac{1}{5^2} + \dots \right] \cdot g \cdot 4kTg_{m1}$$

$$SW(f) = \frac{4}{p} d(w_{LO}) + \frac{4}{3p} d(3w_{LO}) + \dots$$

$$\overline{i_{noise-M1}^2}(w_{IF}) = 4 \cdot g \cdot 4kTg_{m1}$$

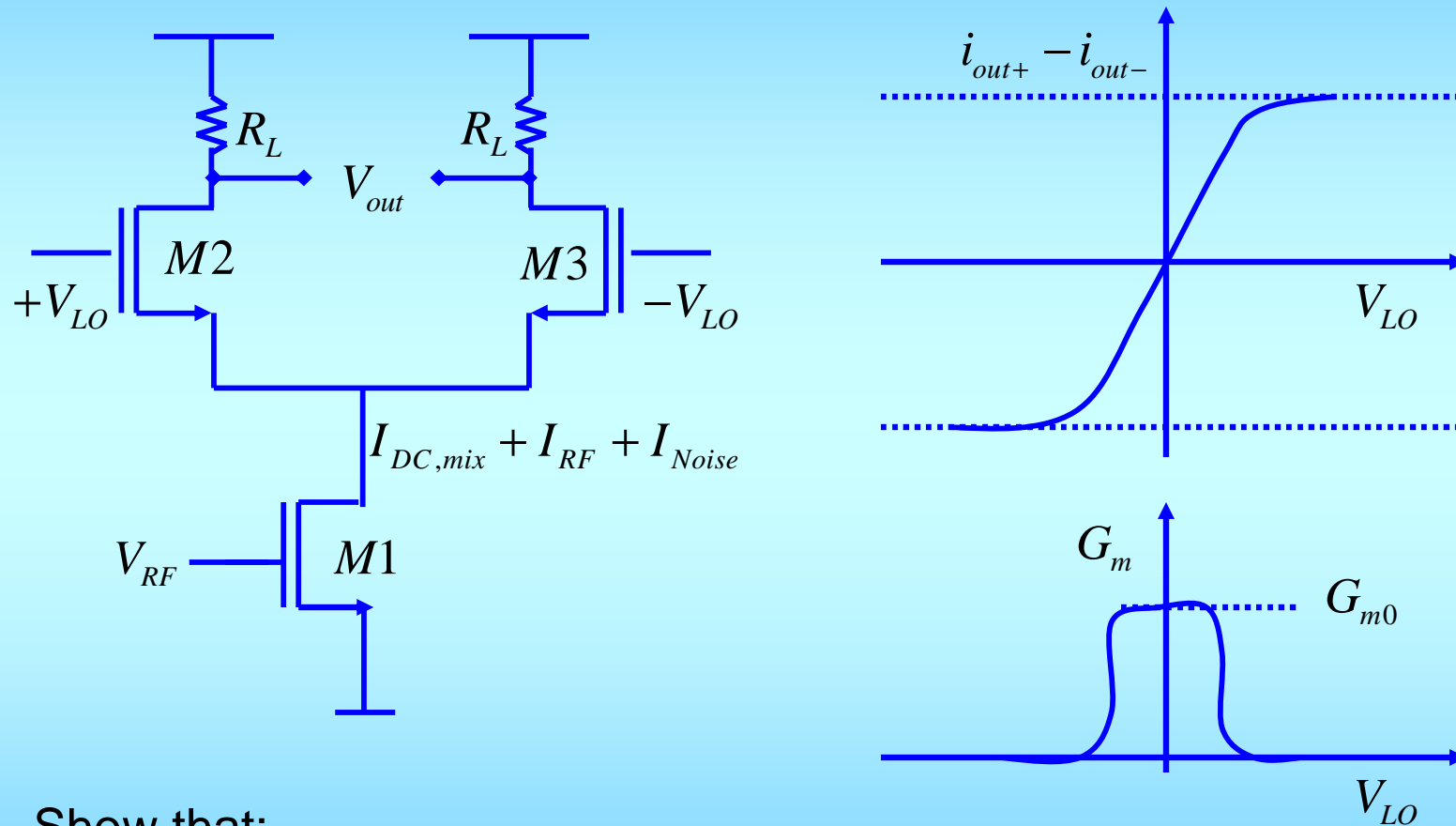
Mixer Noise Analysis: Switch Noise

- Noise Analysis of Heterodyne Mixer (switch noise):



Mixer Noise Analysis: Switch Noise

- Noise Analysis of Heterodyne Mixer (switch noise):

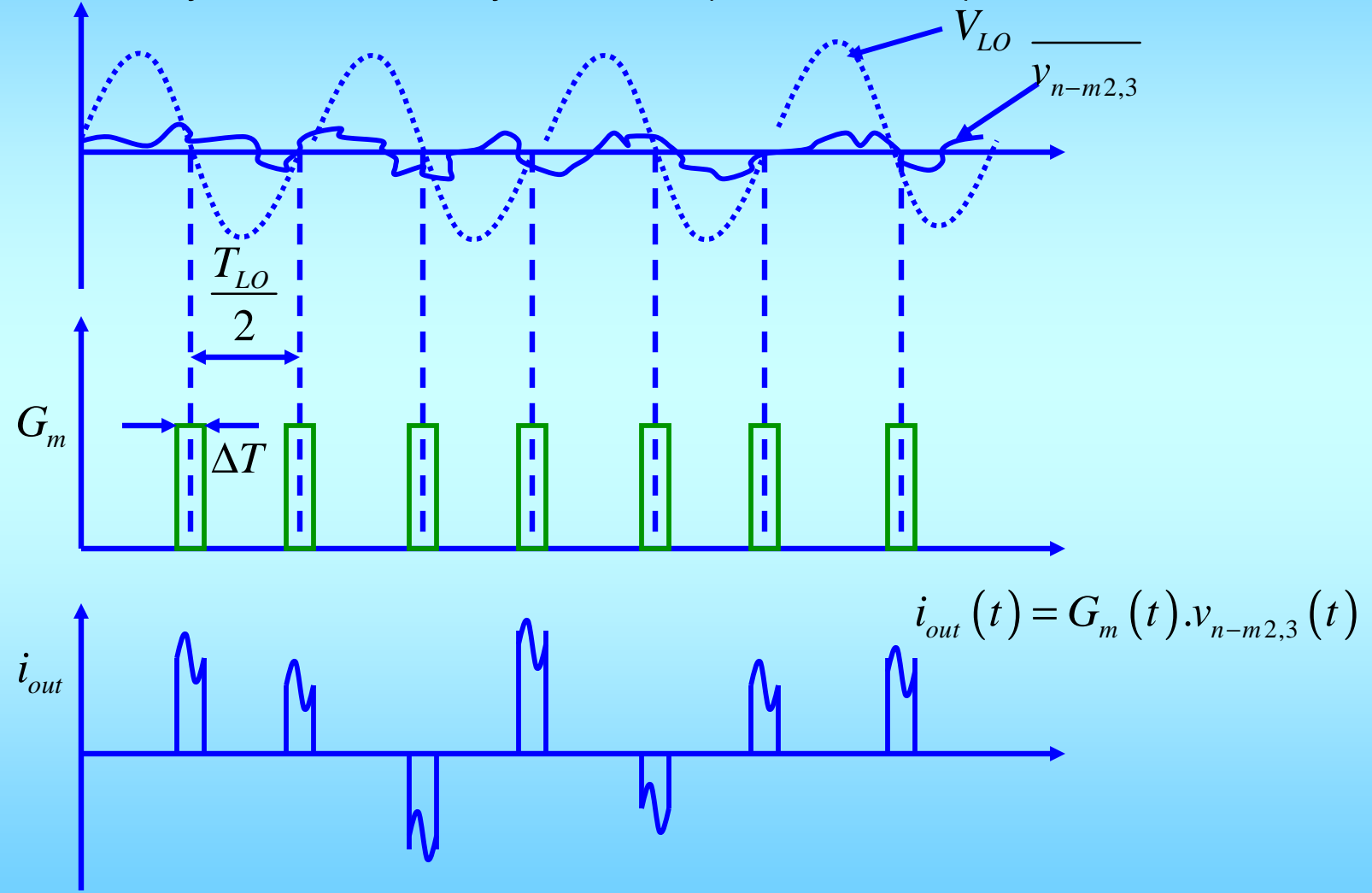


- Show that:

$$G_m = g_{m2} = g_{m3} = g_{m2,3} \approx \frac{2 \cdot I_{DC,mix}}{\Delta V}$$

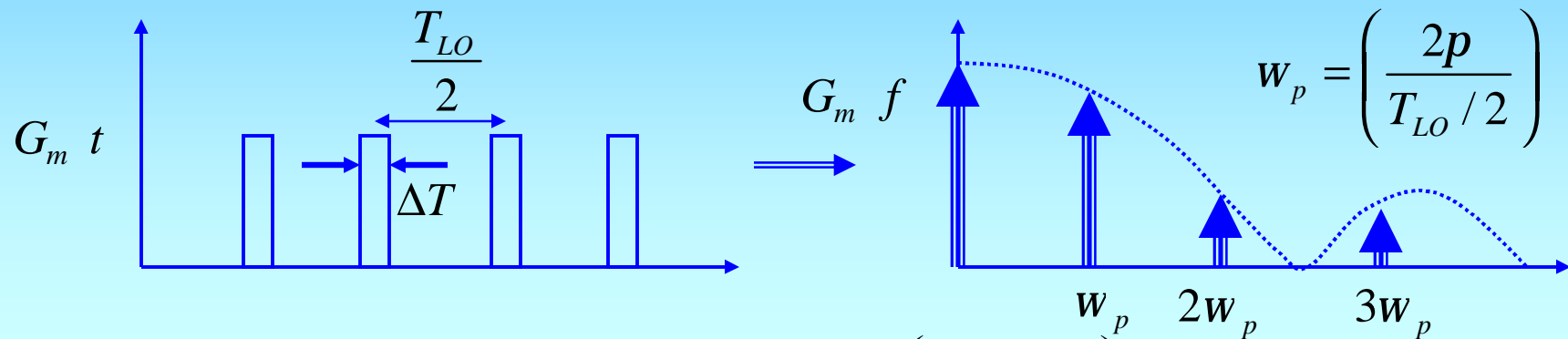
Mixer Noise Analysis: Switch Noise

- Noise Analysis of Heterodyne Mixer (switch noise) cont...:



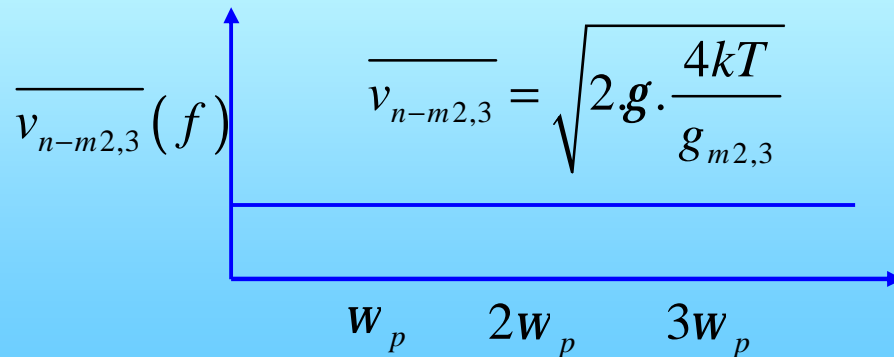
Mixer Noise Analysis: Switch Noise

- Noise Analysis of Heterodyne Mixer (switch noise) cont...:



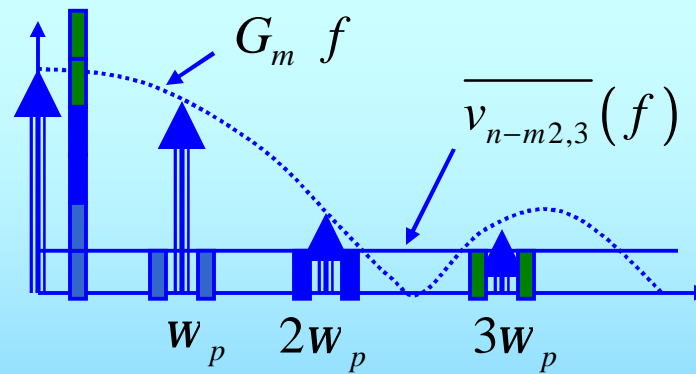
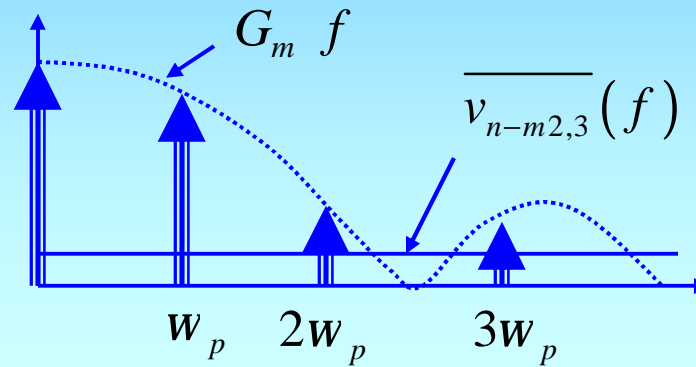
$$G_m(t) = G_{m0} \cdot \left(\frac{\Delta T}{T_{LO}/2}\right) + \frac{1}{\left(\frac{T_{LO}}{2}\right)} \sum_{k=1}^{\infty} \Delta T \cdot G_{m0} \cdot \frac{\text{Sin}\left(k \cdot \frac{\Delta T \omega_p}{2}\right)}{\left(k \cdot \frac{\Delta T \omega_p}{2}\right)} \cdot 2 \text{Cos}(k \omega_p t)$$

$$\overline{v_{n-m2,3}} = \sqrt{v_{n-m2}^2 + v_{n-m3}^2}$$



Mixer Noise Analysis: Switch Noise

- Noise Analysis of Heterodyne Mixer (switch noise) cont...:



$$\overline{i_{noise-M2,3}^2}(w_{IF}) = \frac{1}{\left(\frac{T_{LO}}{2}\right)} \cdot G_{m0}^2 \cdot \Delta T \cdot \overline{v_{n-m2,3}^2}$$

Mixer Noise Analysis: Switch Noise

- Noise Analysis of Heterodyne Mixer (switch noise) cont...:

$$\overline{i_{noise-M2,3}^2}(\omega_{IF}) = \frac{1}{\left(\frac{T_{LO}}{2}\right)} \cdot G_{m0}^2 \cdot \Delta T \cdot \overline{v_{n-m2,3}^2} \quad G_m = g_{m2} = g_{m3} = g_{m2,3} \approx \frac{2 \cdot I_{DC,mix}}{\Delta V}$$

$$G_{m0} = \frac{2I_{DC,mix}}{\Delta V} \quad \Delta V = Slope \cdot \Delta T \quad V_{LO}(t) = A_{LO} \cos(\omega_{LO} t)$$

$$Slope \Big|_{\omega_{LO} t = 90} = \left[\frac{dV_{LO}(t)}{dt} \right]_{\omega_{LO} t = 90} = A_{LO} \omega_{LO} \quad \overline{v_{n-m2,3}^2} = \sqrt{2 \cdot g \cdot \frac{4kT}{g_{m2,3}}}$$

$$\overline{i_{noise-M2,3}^2}(\omega_{IF}) = \frac{1}{T_{LO}/2} \cdot G_{m0}^2 \cdot \Delta T \cdot \overline{v_{n-m2,3}^2} = \frac{1}{T_{LO}/2} \cdot G_{m0}^2 \cdot \Delta T \cdot \left(2 \cdot g \cdot \frac{4kT}{g_{m2,3}} \right)$$

$$= \frac{1}{T_{LO}/2} \cdot G_{m0} \cdot \Delta T \cdot (2 \cdot g \cdot 4kT) = \frac{1}{T_{LO}/2} \cdot \frac{2 \cdot I_{DC,mix}}{\Delta V} \cdot \Delta T \cdot (2 \cdot g \cdot 4kT)$$

$$= \frac{2I_{DC,mix}}{T_{LO}/2} \cdot (2 \cdot g \cdot 4kT) \cdot \frac{\Delta T}{\Delta V} = \frac{2I_{DC,mix}}{T_{LO}/2} \cdot (2 \cdot g \cdot 4kT) \cdot \frac{1}{A_{LO} \omega_{LO}}$$

$$= 4 \cdot g \cdot 4kT \left(\frac{I_{DC,mix}}{p A_{LO}} \right) \leftarrow \text{Total Noise Contribution due to switches M2 and M3}$$

Mixer Noise Analysis: Total Noise

- Noise Analysis of Heterodyne Mixer (total noise):

$$\overline{v_{noise-RL}^2} = 4kT(2R_L) \quad g_{m-short} = \frac{dI_{DS-short}}{dV_{GS}} = \frac{1}{2}WC_{ox}v_{sat} = \frac{I_{DS-short}}{(V_{GSQ} - V_{T0})}$$

$$\overline{i_{noise-M1}^2}(W_{IF}) = 4 \cdot g \cdot 4kT g_{m1} = 4 \cdot g \cdot 4kT \cdot \frac{I_{DC,mix}}{(V_{GSQ} - V_{T0})}$$

$$\overline{i_{noise-M2,3}^2}(W_{IF}) = 4 \cdot g \cdot 4kT \left(\frac{I_{DC,mix}}{p A_{LO}} \right)$$

$$\overline{v_{noise-MIX}^2}(W_{IF}) = \overline{v_{noise-RL}^2} + R_L^2 \overline{i_{noise-M1}^2} + R_L^2 \overline{i_{noise-M2,3}^2}$$

$$\overline{v_{noise-MIX}^2}(W_{IF}) = 4kTR_L \left\{ 1 + 4 \cdot g \cdot \frac{I_{DC,mix}}{(V_{GSQ} - V_{T0})} \cdot R_L + 4 \cdot g \cdot \frac{I_{DC,mix}}{p A_{LO}} \cdot R_L \right\}$$

Mixer Noise Analysis: Total Noise

- Noise Analysis of Heterodyne Mixer (total noise):

$$\overline{v_{noise-MIX}^2} (W_{IF}) = 4kTR_L \left\{ 1 + 4.g \cdot \frac{I_{DC,mix}}{(V_{GSQ} - V_{T0})} \cdot R_L + 4.g \cdot \frac{I_{DC,mix}}{p A_{LO}} \cdot R_L \right\}$$

$(V_{GSQ} - V_{T0}) \uparrow \Rightarrow$ M1 linearity \uparrow and noise \downarrow

$A_{LO} \uparrow \Rightarrow$ noise contribution from M2/M3 \downarrow

