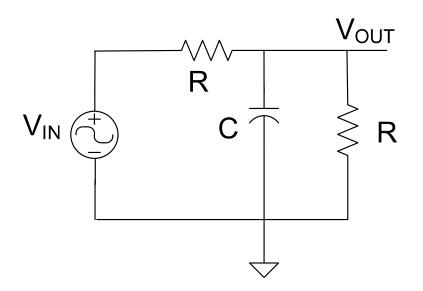
EE 230 Lecture 5

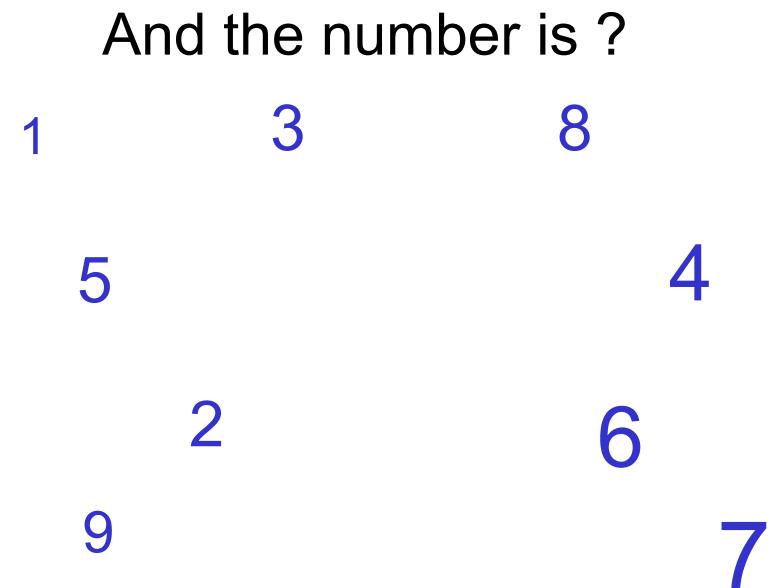
Linear Systems

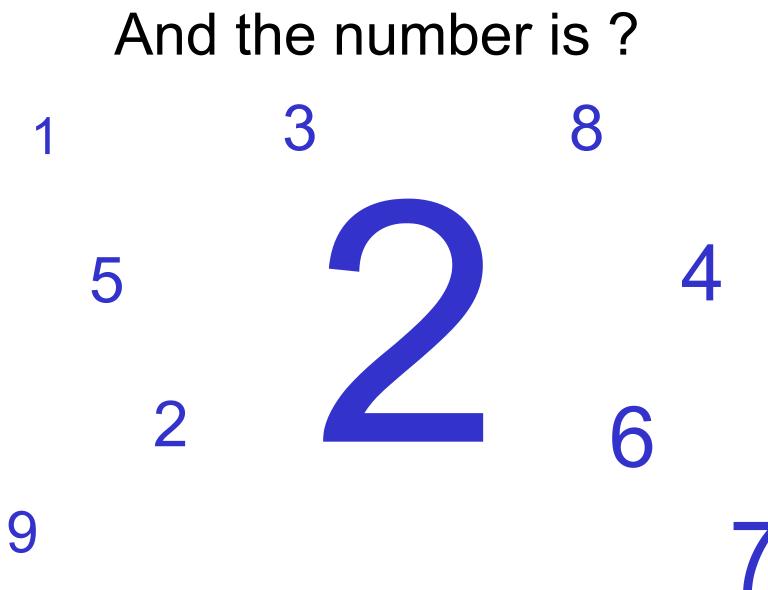
- Poles/Zeros/Stability
- Stability

Quiz 4

Obtain the transfer function T(s) for the circuit shown. $\left(T(s) = \frac{V_{OUT}(s)}{V_{IN}(s)}\right)$





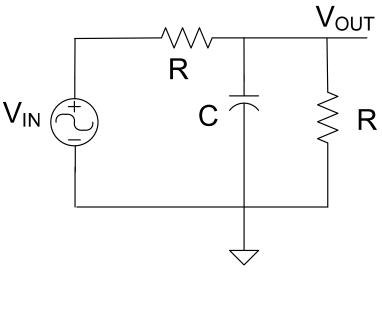


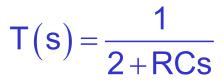
.

Quiz 4

Obtain the transfer function T(s) for the circuit shown. $\left(T(s) = \frac{V_{OUT}(s)}{V_{INI}(s)}\right)$

Solution:





Review from Last Time Test Equipment in the EE 230 Laboratory



- The documentation for the operation of this equipment is extensive
- · Critical that user always know what equipment is doing
- Consult the users manuals and specifications whenever unsure

Review from Last Time Key Theorem:

Theorem: The steady-state response of a linear network to a sinusoidal excitation of $V_{IN} = V_M \sin(\omega t + \gamma)$ is given by

$$V_{OUT}(t) = V_{m} |T(j\omega)| sin(\omega t + \gamma + \angle T(j\omega))$$

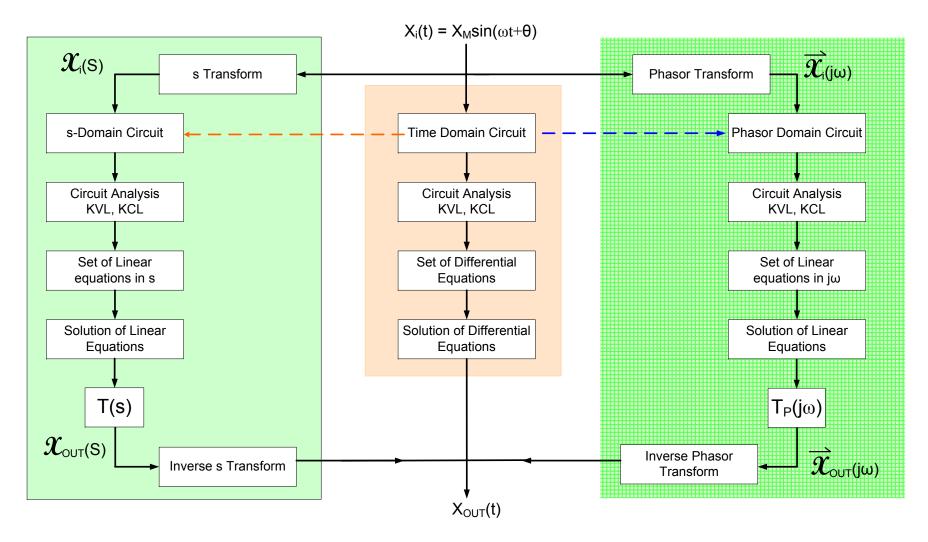
This is a very important theorem and is one of the major reasons phasor analysis was studied in EE 201

The sinusoidal steady state response is completely determined by $T(j\omega)$

The sinusoidal steady state response can be written by inspection from the

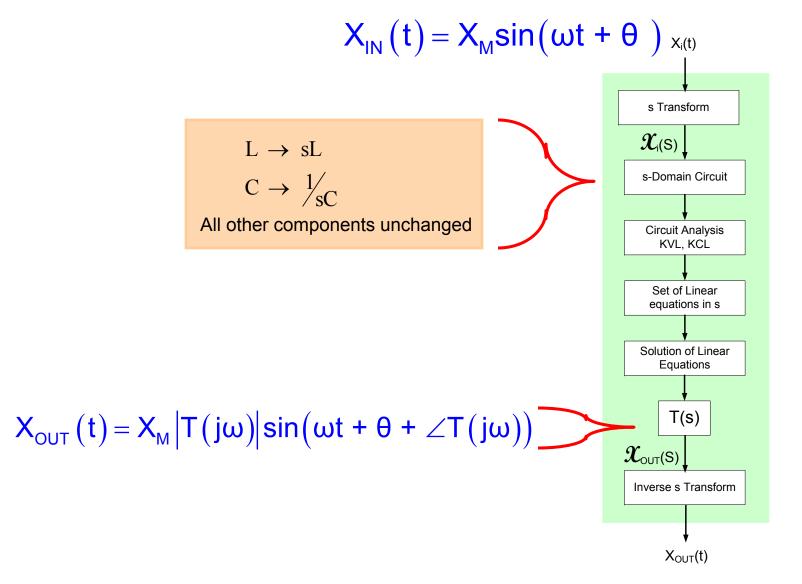
$$|T(j\omega)|$$
 and $\angle T(j\omega)$ plots
 $T(s)|_{s=i\omega} = T_P(j\omega)$

Review from Last Time Formalization of sinusoidal steady-state analysis

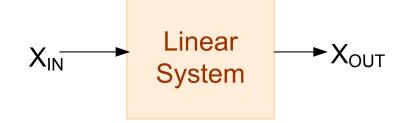


Review from Last Time

Formalization of sinusoidal steady-state analysis - Summary s-domain The Preferred Approach



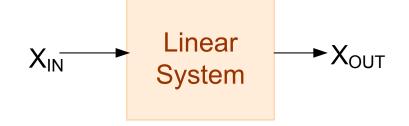
Gain, Frequency Response, Transfer Function



Assume the transfer function is T(s)

- Linear system can be called an amplifier, filter, or simply a linear system
- Gain is, by definition, |T(jω)| (tells how sinusoids propagate through system)
- Arg $(T(j\omega))$ is, by definition the phase of system (gives phase shift of sinusoid)
- Plots of $|T(j\omega)|$ and Arg $(T(j\omega))$ widely used to characterize the frequency response of the system

Gain, Frequency Response, Transfer Function



Assume the transfer function is T(s)

Transfer functions of linear system with finite number of lumped elements is a rational fraction in s with real coefficients

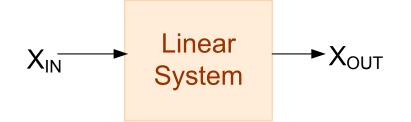
$$T(s) = \frac{\sum_{i=0}^{m} a_i s^i}{\sum_{i=0}^{n} b_i s^i}$$

For any realizable system, $m \leq n$

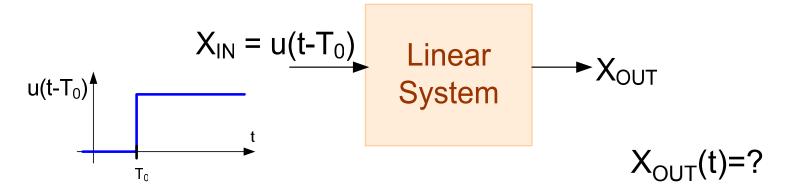
Order of transfer function is equal, by definition, to n

n often referred to as the order of the system

Step Response of First-Order Networks



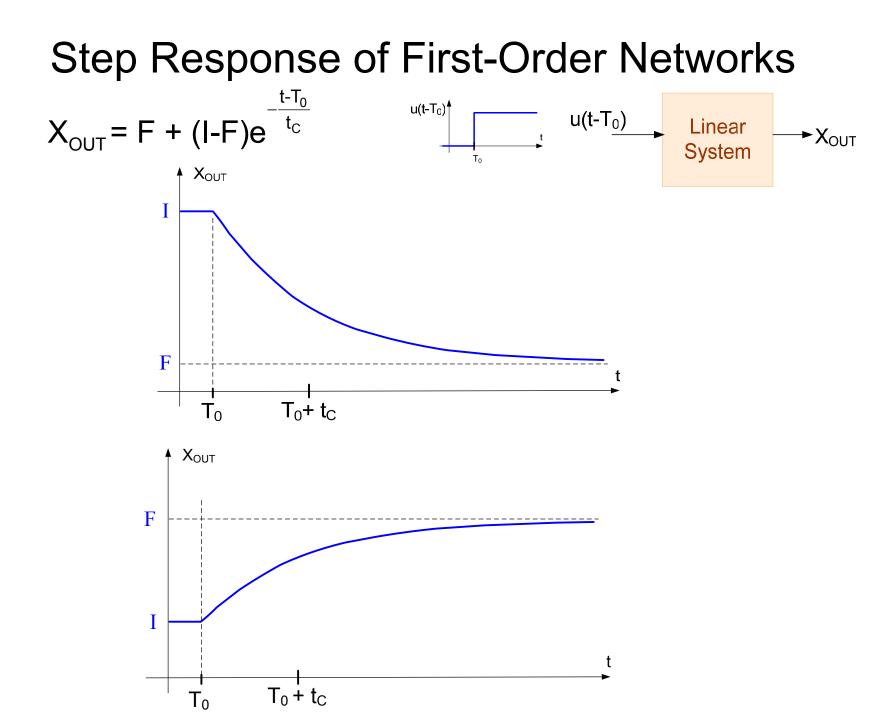
Many times interested in the step response of a linear system when the system is first-order

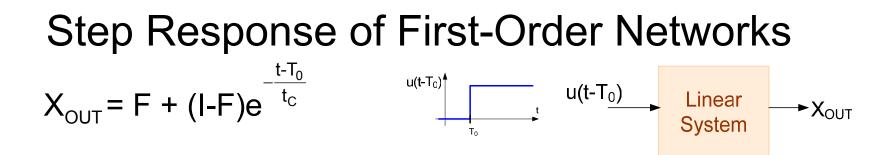


For any first-order linear system, the unit step response is given by

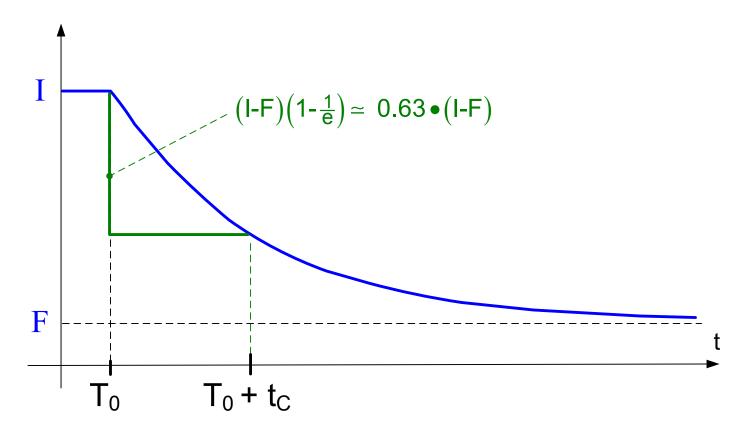
$$X_{OUT} = F + (I-F)e^{-\frac{t-1}{t_c}}$$

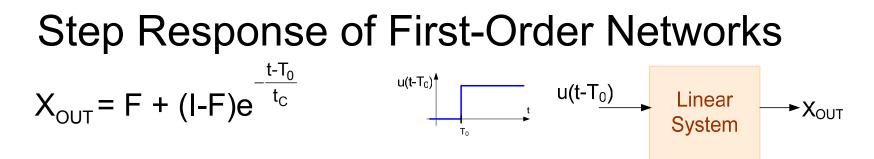
I is the intital value, F is the final value and t_c is the time constant





Effects of time constant shown for decaying step response





Observe the step response completely determined by the 3 parameters, $\{I, F, t_C\}$

In the frequency domain, any first-order system can be expressed as

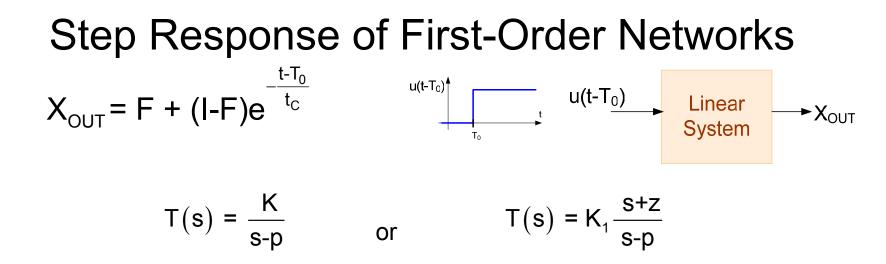
$$T(s) = \frac{N(s)}{s-p}$$

where N(s) is either a zero-order of first-order polynomial in S

$$T(s) = \frac{K}{s-p}$$
 or $T(s) = K_1 \frac{s-z}{s-p}$

Note the first-order transfer function is characterized by either 2 parameters {k,p} or 3 parameters {K₁, z, p}

Thus the 3 step response parameters must be expressible in terms of the 2 or 3 parameters of the transfer function



Thus the 3 step response parameters must be expressible in terms of the 2 or 3 parameters of the transfer function

 $t_{c} = -p^{-1}$

The expressions for F and I are left to the student

Often can be obtained by inspection from the circuit

Step Response of First-Order Networks

Example:

Obtain the step response of the circuit shown if the step is applied at time T=1msec and prior to $V_{OUT}(t)=0$ for t<1msec. Assume R=1K, C=0.1uF

Solution:

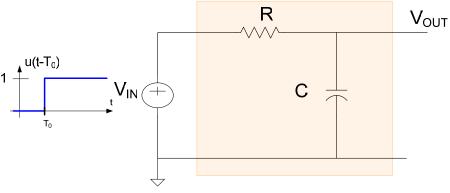
$$T(s) = \frac{1}{1 + RCs}$$
$$T(s) = \frac{\frac{1}{RC}}{\frac{1}{s + \frac{1}{RC}}}$$

This is first order and of the form:

$$T(s) = \frac{\kappa}{s-p}$$
 $\therefore p = -\frac{1}{RC}$ $t_c = -p^{-1} = RC$

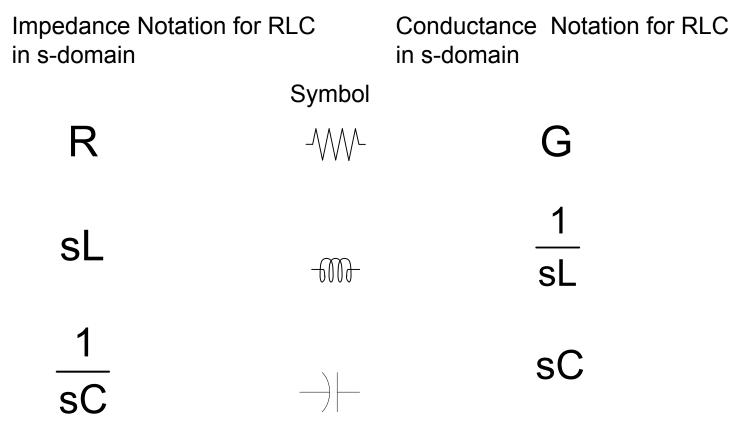
Thus, the output can be expressed as:

$$V_{OUT} = F + (I-F)e^{-\frac{t-T_0}{t_c}}$$



F=1V
I=1V
$$V_{OUT} = 1 + (-1)e^{-\frac{t-.001}{RC}}$$

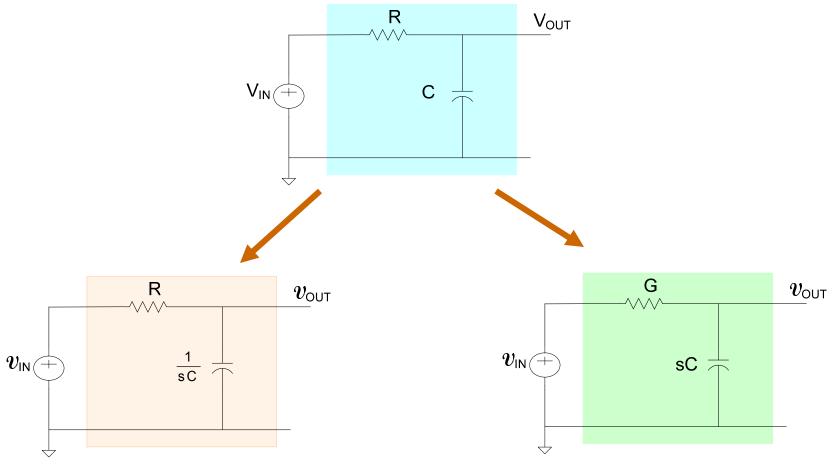
 $V_{OUT} = 1 - e^{-\frac{t-.001}{RC}}$



Conductance = 1/Impedance

Symbols the same, often more convenient to use conductance notation

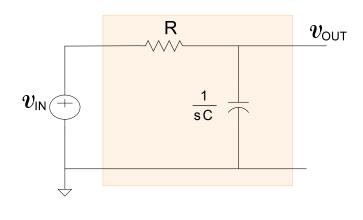
Example:

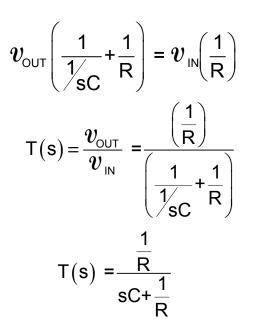


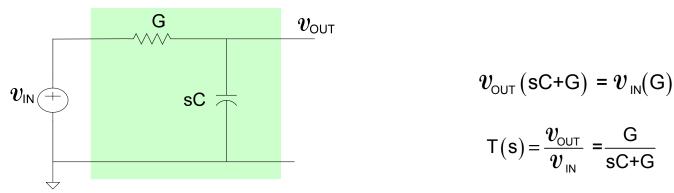
s-domain with impedance notation

s-domain with conductance notation

Example:







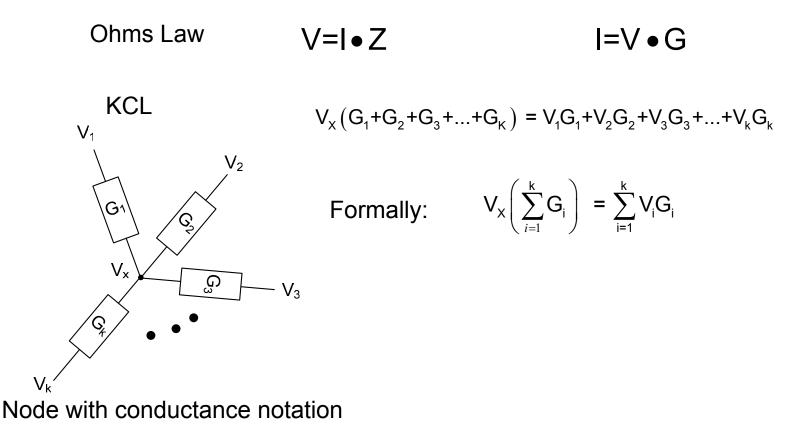
Analysis, using KCL, often much faster using conductance notation

Circuit Analysis with Impedance Notation (Z) and Conductance Notation (G)

Ohms Law $V=1 \bullet 7$ l=V ●G $(V_{x}-V_{1})\left(\frac{1}{Z_{1}}\right)+(V_{x}-V_{2})\left(\frac{1}{Z_{2}}\right)+(V_{x}-V_{3})\left(\frac{1}{Z_{2}}\right)+...+(V_{x}-V_{k})\left(\frac{1}{Z_{k}}\right)=0$ **KCL** $V_{X}\left(\frac{1}{Z_{1}} + \frac{1}{Z_{2}} + \frac{1}{Z_{2}} + \dots + \frac{1}{Z_{k}}\right) = V_{1}\left(\frac{1}{Z_{1}}\right) + V_{2}\left(\frac{1}{Z_{2}}\right) + V_{3}\left(\frac{1}{Z_{2}}\right) + \dots + V_{k}\left(\frac{1}{Z_{k}}\right)$ V₁ Formally: $V_{x}\left(\sum_{i=1}^{k}\frac{1}{Z_{i}}\right) = \sum_{i=1}^{k}V_{k}\frac{1}{Z_{i}}$ Often faster to use the second form V_x \mathcal{N} V_2

Node with impedance notation

Circuit Analysis with Impedance Notation (Z) and Conductance Notation (G)

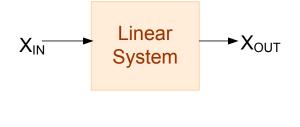


KCL is often the fastest way to analyze electronic circuits Why?

Conductance notation is often much less cumbersome than impedance notation when analyzing electronic circuits Why?

For any linear system, T(s) can be expressed as

 $T(s) = \frac{\sum_{i=0}^{n} a_i s^i}{\sum_{i=0}^{n} b_i s^i}$



where $a^{}_i$ and $b^{}_i$ are all real, $b^{}_n \ \neq \ 0$, $a^{}_m \ \neq \ 0$, and $n \geq m$

Can always make b_n=1

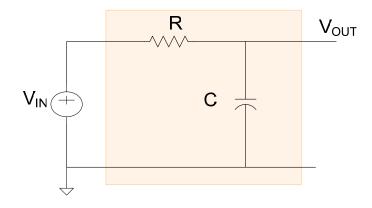
Numerator often termed N(s) Denominator often termed D(s)

$$T(s) = \frac{\sum_{i=0}^{m} a_i s^i}{\sum_{i=0}^{n} b_i s^i} = \frac{N(s)}{D(s)}$$

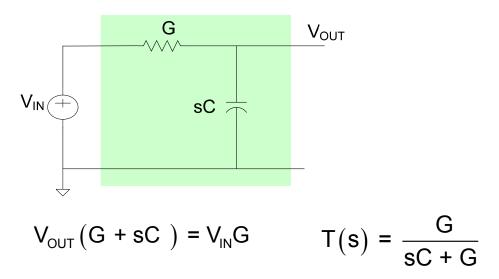
Definition: The roots of D(s) are the poles of T(s) and the roots of N(s) are the zeros of T(s)

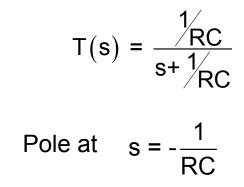
The poles of T(s) are often termed the poles of the system

Example: Determine the poles and zeros of the following circuit where the input and output variables are indicated



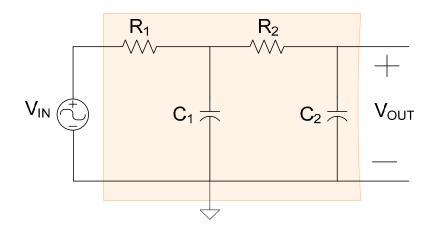
Draw s-domain circuit using conductance notation



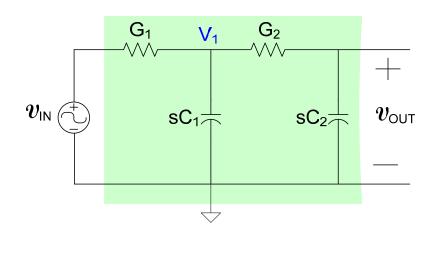




Example: Determine the poles and zeros of the following circuit where the input and output variables are indicated



Draw s-domain circuit using "conductance" notation

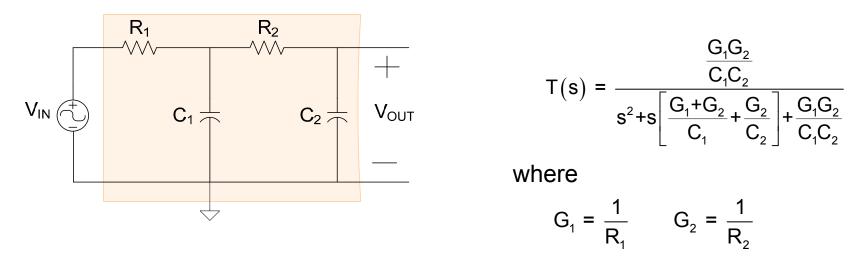


By KCL $V_1(G_1+G_2+sC_1) = V_{IN}G_1 + V_{OUT}G_2$ $V_{OUT}(G_2+sC_2) = V_1G_2$

Solving, obtain:

$$T(s) = \frac{\frac{G_{1}G_{2}}{C_{1}C_{2}}}{s^{2}+s\left[\frac{G_{1}+G_{2}}{C_{1}}+\frac{G_{2}}{C_{2}}\right]+\frac{G_{1}G_{2}}{C_{1}C_{2}}}$$

Example: Determine the poles and zeros of the following circuit where the input and output variables are indicated



No zeros

Two poles obtained by solving quadratic equation

$$p_{1} = -\left(\frac{1}{2}\left[\frac{G_{1}+G_{2}}{C_{1}}+\frac{G_{2}}{C_{2}}\right]\right) + \frac{1}{2}\sqrt{\left[\frac{G_{1}+G_{2}}{C_{1}}+\frac{G_{2}}{C_{2}}\right]^{2}-4\frac{G_{1}G_{2}}{C_{1}C_{2}}}$$
$$p_{2} = -\left(\frac{1}{2}\left[\frac{G_{1}+G_{2}}{C_{1}}+\frac{G_{2}}{C_{2}}\right]\right) - \frac{1}{2}\sqrt{\left[\frac{G_{1}+G_{2}}{C_{1}}+\frac{G_{2}}{C_{2}}\right]^{2}-4\frac{G_{1}G_{2}}{C_{1}C_{2}}}$$

Example: Determine the poles and zeros of the following system

$$\mathsf{T}(\mathsf{s}) = \frac{\mathsf{s}+4}{\mathsf{s}^2+9\mathsf{s}+8}$$

write in factored form as

$$T(s) = \frac{s+4}{(s+1)(s+8)}$$

zeros: {s = -4}

poles: $\{s = -1, s = -8\}$

End of Lecture 5