EE 508 Lecture 5

- Dead Networks
- Root Characterizations
- Scaling, Normalization and Transformations
- Degrees of Freedom and Systematic Design

Filter Concepts and Terminology

- 2-nd order polynomial characterization
- Biquadratic Factorization
- Op Amp Modeling
- Stability and Instability
- Roll-off characteristics
- Distortion
- Dead Networks
 - Root Characterization
 - Scaling, normalization, and transformation

Dead Networks



The "dead network" of any linear circuit is obtained by setting ALL independent sources to zero.

- Replace independent current sources with opens
- Replace independent voltage sources with shorts
- Dependent sources remain intact

D(s) is characteristic of the dead network and is independent of where the excitation is applied or where the response is measured

D(s) is the same for ALL transfer functions of a given "dead network"

Dead Networks

Example:





D(s) = 1+RCs





Dead Networks





$$\frac{v_{\text{OUT}}}{i_{\text{IN}}} = T(s) = \frac{R}{1 + RCs}$$
$$D(s) = 1 + RCs$$

$$\frac{i_{OUT}}{i_{IN}} = T(s) = \frac{RCs}{1+RCs}$$
$$D(s) = 1+RCs$$

$$\frac{v_{\text{OUT}}}{i_{\text{IN}}} = T(s) = \frac{1}{Cs}$$
$$D(s) = Cs$$

Note: This has a different dead network!



This is an important observation. Why is it true?

Plausibility argument:

Consider a network with only admittance elements and independent current sources

At node k, can write the equation

$$\sum_{\substack{i=1\\i\neq k}}^{n} \mathbf{Y}_{ki} \left(\mathbf{V}_{k} - \mathbf{V}_{i} \right) = \mathbf{I}_{k}$$







Plausibility argument:

Doing this at each node results in the set of equations

$$\begin{bmatrix} Y_{11} & Y_{12} & \dots & Y_{1n} \\ Y_{21} & Y_{22} & \dots & Y_{2n} \\ \vdots & & & & \\ Y_{n1} & Y_{n2} & \dots & Y_{nn} \end{bmatrix} \bullet \begin{bmatrix} V_1 \\ V_2 \\ \vdots \\ \vdots \\ V_n \end{bmatrix} = \begin{bmatrix} I_1 \\ I_2 \\ \vdots \\ \vdots \\ V_n \end{bmatrix}$$

In matrix form

$$Y \bullet V = I$$

The nodal voltages are given by

 $V = Y^{-1} \bullet I$





Plausibility argument:

 $V = Y^{-1} \bullet I$

The nodal voltage V_k in this solution is given by the ratio of two determinates where the one in the numerator is obtained by replacing the kth column with the excitation vector and the one in the denominator is the determinate of the indefinite admittance matrix **Y**

Note the denominator is the same for all nodal voltages and is independent of the excitations – that is, it is dependent only upon the dead network







Plausibility argument:

Note the denominator is the same for all nodal voltages and is independent of the excitations – that is, it is dependent only upon the dead network

Thus all branch voltages and all branch currents have the same denominator and this (after multiplying through by the correct power of s to make V_k a rational fraction) is the characteristic polynomial D(s)

This concept can be extended to include independent voltage sources as well as dependent sources



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Root Characterization

Scaling, normalization, and transformation

Root characterization in s-plane (for complex-conjugate roots)



For high Q, θ is small

Root characterization in s-plane (for complex-conjugate roots)



roots located at

Filter Concepts and Terminology

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Scaling, normalization, and transformation

Scaling, Normalization and Transformations

- Frequency scaling
- Frequency Normalization
 - Impedance scaling
 - Transformations
 - LP to BP
 - LP to HP
 - LP to BR

Scaling, Normalization and Transformations

Frequency normalization:

Frequency scaling:

 $s = \omega_0 s_n$

 $s_n = \frac{s}{\omega_0}$

Purpose:

 ω_0 independent approximations

 ω_0 independent synthesis

Simplifies analytical expressions for T(s)

Simplifies component values in synthesis

Use single table of normalized filter circuits for given normalized approximating function

Note: The normalization subscript "n" is often dropped

$$T(s) = \frac{6000}{s + 6000}$$



Normalized transfer function:

$$T_n(s_n) = \frac{1}{s_n + 1}$$



$$T_n(s_n) = \frac{1}{s_n + 1}$$



Synthesis of normalized function







Frequency scaling transfer function by ω_0

 $s = \omega_0 s_n$



Frequency scaling circuit by ω_0 (actually magnitude of ω_0) (scale all energy storage elements in circuit)





Frequency scaling / normalization does not change the shape of the transfer function, it only scales the frequency axis linearly

The frequency scaled circuit can be obtained from the normalized circuit simply by scaling the frequency dependent impedances (up or down) by the scaling factor

This makes the use of filter design tables for the design of lowpass filters practical whereby the circuits in the table all have a normalized band edge of 1 rad/sec.



Frequency normalization/scaling

Example: Table for passive LC ladder Butterworth filter with 3dB band edge of 1 rad/sec and resistive source/load terminations

BUTT



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	n	R	c.	L	C.	L.		
			-1	2	3	4		
	2	L.7079 L.1111 L.7500 L.4206 I.46667 2.3000 2.5030 3.3333 5.0000 L0.0000 LNF.	1.4142 1.0353 0.6483 0.5637 0.5657 0.4483 0.3419 0.2447 0.1557 0.1557 0.1557 0.1557 0.1557	1.4142 1.6352 2.1213 2.4397 2.8284 3.3461 4.0951 5.3126 7.7067 14.8130 0.7071				
	3	1.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.1000 INF.	1.0000 0.8342 0.3442 0.3442 1.0225 1.1811 1.4254 1.4254 1.4380 2.6687 5.1672 1.5000	2,0000 1,6332 1,3840 1,1642 0,9550 0,6062 0,4396 0,2862 0,1377 1,333	1.0000 1.5994 1.9259 2.2774 2.71024 3.2612 5.3634 7.9102 15.4554 0.5000			
	4	1.0000 1.1111 1.2500 1.6557 2.0000 2.5000 3.3333 5.0000 1.0000 1.4000 1.45.	0.7654 0.4657 0.3987 0.2693 0.2175 0.1692 0.1237 0.0906 0.0392 1.5307	1.8478 1.5924 1.6946 1.8618 2.1029 2.4524 2.4524 2.9858 3.8828 5.6835 11.0962 1.5772	1.8475 1.7439 1.5110 1.2913 1.0824 0.8826 0.6911 0.5072 0.3307 0.1616 1.0824	0.7654 1.4690 1.8109 2.1752 2.6131 3.1868 4.0094 4.1381 7.9397 15.6421 0.3827		
	n	1/R_8	L ₁	с ₂	1.3	C4		
			-ŵ-	1.5 1.02				↓ ∞

I EVEN

r 000

Frequency normalization/scaling

The frequency scaled circuit can be obtained from the normalized circuit simply by scaling the frequency dependent impedances (up or down) by the scaling factor

Component denormalization by factor of ω_0



Component values of energy storage elements are scaled down by a factor of ω_0

Desgin Strategy

Theorem: A circuit with transfer function T(s) can be obtained from a circuit with normalized transfer function $T_n(s_n)$ by denormalizing all frequency dependent components.

C --> C/ω_o L --> L/ω_o

Example: Design a V-V passive 3rd-order Lowpass Butterworth filter with a 3-db band-edge of 1K rad/sec and equal source and load terminations.



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Is this solution practical?

Some component values are too big and some are too small !

Filter Concepts and Terminology

- Frequency scaling
- Frequency Normalization
- Impedance scaling
 - Transformations
 - LP to BP
 - LP to HP
 - LP to BR

Impedance Scaling

Impedance scaling of a circuit is achieved by multiplying ALL impedances in the circuit by a constant



 $A \longrightarrow \begin{array}{c} \theta A & \text{for transresistance gain} \\ A & \text{for dimensionless gain} \\ A/\theta & \text{for transconductance gain} \end{array}$

Impedance Scaling

Theorem: If all impedances in a circuit are scaled by a constant θ , then

- a) All dimensionless transfer functions are unchanged
- b) All transresistance transfer functions are scaled by θ
- c) All transconductance transfer functions are scaled by θ^{-1}

Impedance Scaling



$$\Gamma(s) = \frac{1}{s+1}$$

T(s) is dimensionless

Impedances scaled by $\theta = 10^5$

 $V_{\text{IN}} + I_{\text{IN}} + I_{\text$

Note second circuit much more practical than the first

Example: Design a V-V passive 3rd-order Lowpass Butterworth filter with a band-edge of 1K Rad/Sec and equal source and load terminations.



θR

Is this solution practical?

Some component values are too big and some are too small !

Impedance scale by θ =1000



$$T(s) = K \frac{10^9}{s^3 + 2 \cdot 10^3 s^2 + 2 \cdot 10^6 s + 10^9}$$

Component values more practical

Transformations -LP to BP -LP to HP -LP to BR

It can be shown the standard HP, BP, and BR approximations can be obtained by a frequency transformation of a standard LP approximating function

Will address the LP approximation first, and then provide details about the frequency transformations

Typical approach to filter design

- 1. Transform requirements to a low-pass filter problem
- 2. Obtain normalized low-pass approximating function
- 3. Synthesize circuit to realize normalized approximating function
- 4. Denormalize circuit obtained in step 3
- 5. Impedance scale to obtain acceptable component values
- 6. Transform to desired filter type (LP, BP, BR)



Circuit has two design variables: {R,C}

Circuit has one key controllable performance characteristic: $\omega_0 = \frac{1}{RC}$

If ω_0 is specified for a design, circuit has

2 design variables 1 constraint

1 Degree of Freedom

Performance/Cost strongly affected by how degrees of freedom in a design are used !

Note: We have not discussed the Butterworth approximation yet so some details here will be based upon concepts that will be developed later





Circuit has 5 Degrees of Freedom!



2

How many degrees of freedom remain?



Normalizing by the factor ω_0 , we obtain

$$T(s_n) = \frac{1}{s^2 + s\left(\frac{1}{Q}\right) + 1}$$

Lets now use up the two degrees of freedom in the circuit:

Setting R=R₃=1 obtain the following normalized circuit

Setting R=R₃=1 obtain the following circuit



The two constraints become

$$\omega_0 = \frac{1}{RC} = \frac{1}{C} \qquad \qquad Q = \frac{R_Q}{R} = R_Q$$

This leaves 2 unknowns, R_Q and C and two constraints (i.e. no remaining degrees of freedom)

$$T(s_n) = \frac{1}{s^2 + s\left(\frac{1}{Q}\right) + 1} \qquad \omega_{0n} = 1 \qquad Q_N = \frac{1}{\sqrt{2}}$$

To satisfy the 2 constraints, must now set $R_0 = Q$ C = 1



Denormalized circuit with bandedge of 4 KHz



This has the right transfer function (but unity gain)

Can now do impedance scaling to get more practical component values

R → 0R C → C/0 L → 0L

A good impedance scaling factor may be θ =1000

Denormalized circuit with bandedge of 4 KHz



This has the right transfer function (but unity gain)

To finish the design, preceed or follow this circuit with an amplifier with a gain of 5 to meet the dc gain requirements

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